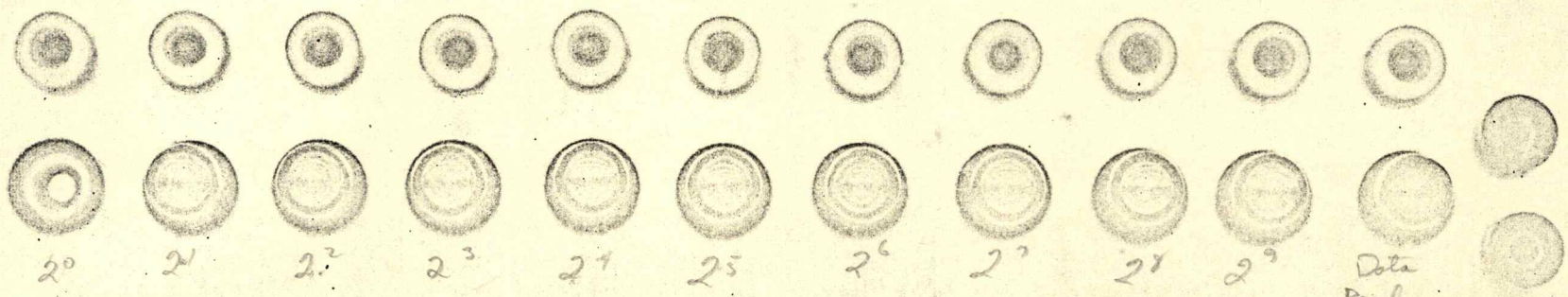


24



28VDC

CIU TEST BOX

20VDC

DATA

GP

MUX

FRAME

CHANNEL

REQ

A
B

2 1

8 4 2 1

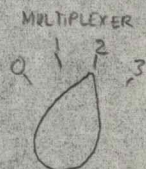
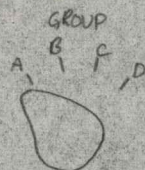
16 8 4 2 1

Priority B

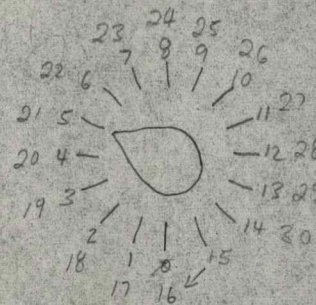
(16-30 Red)



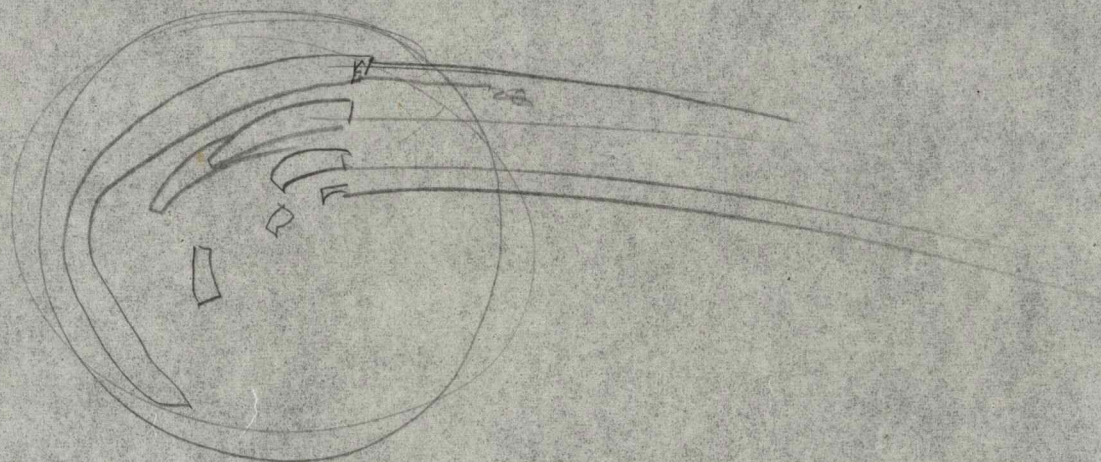
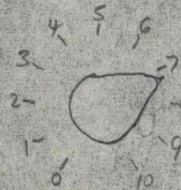
(1-15 Green)

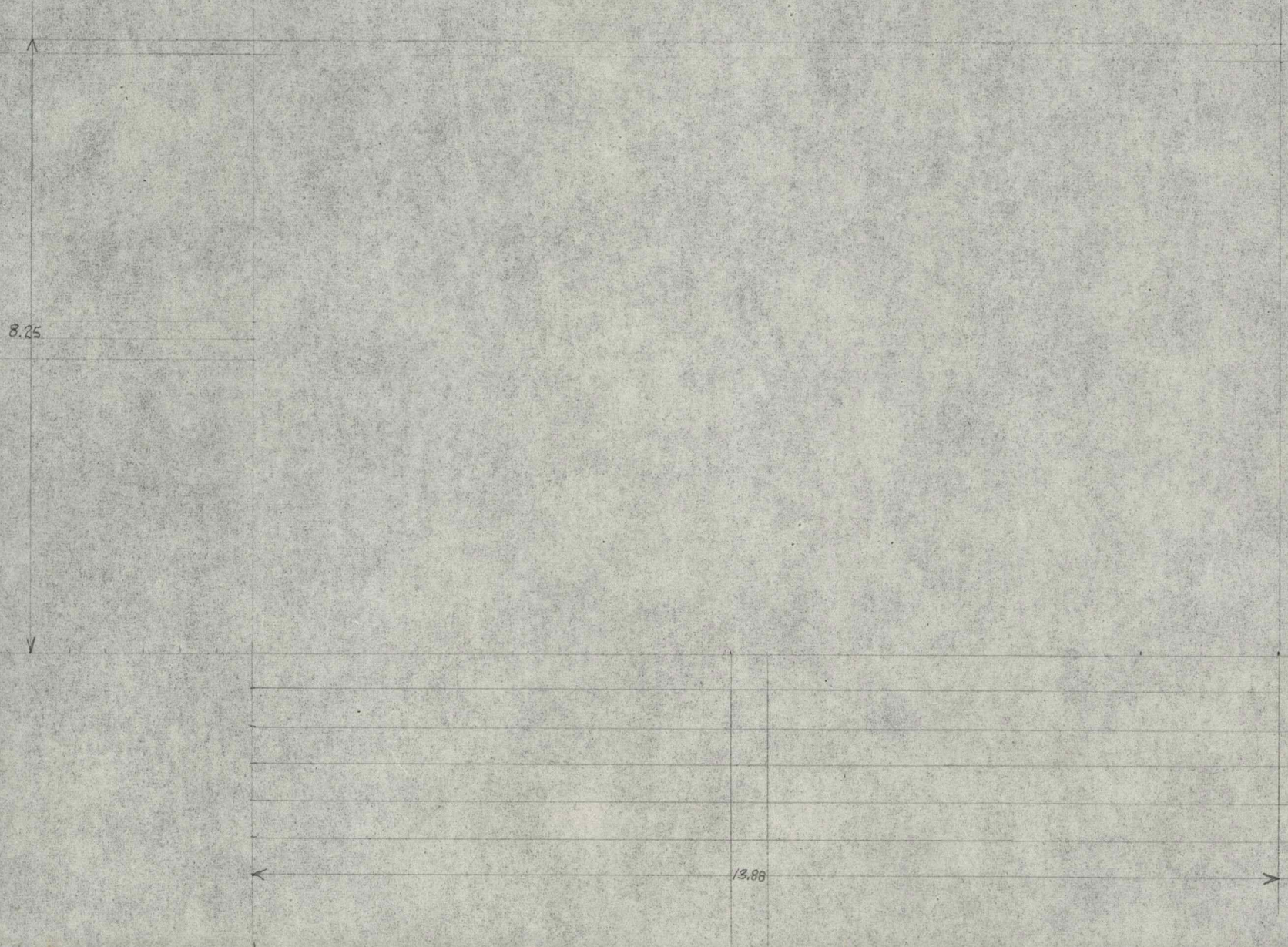


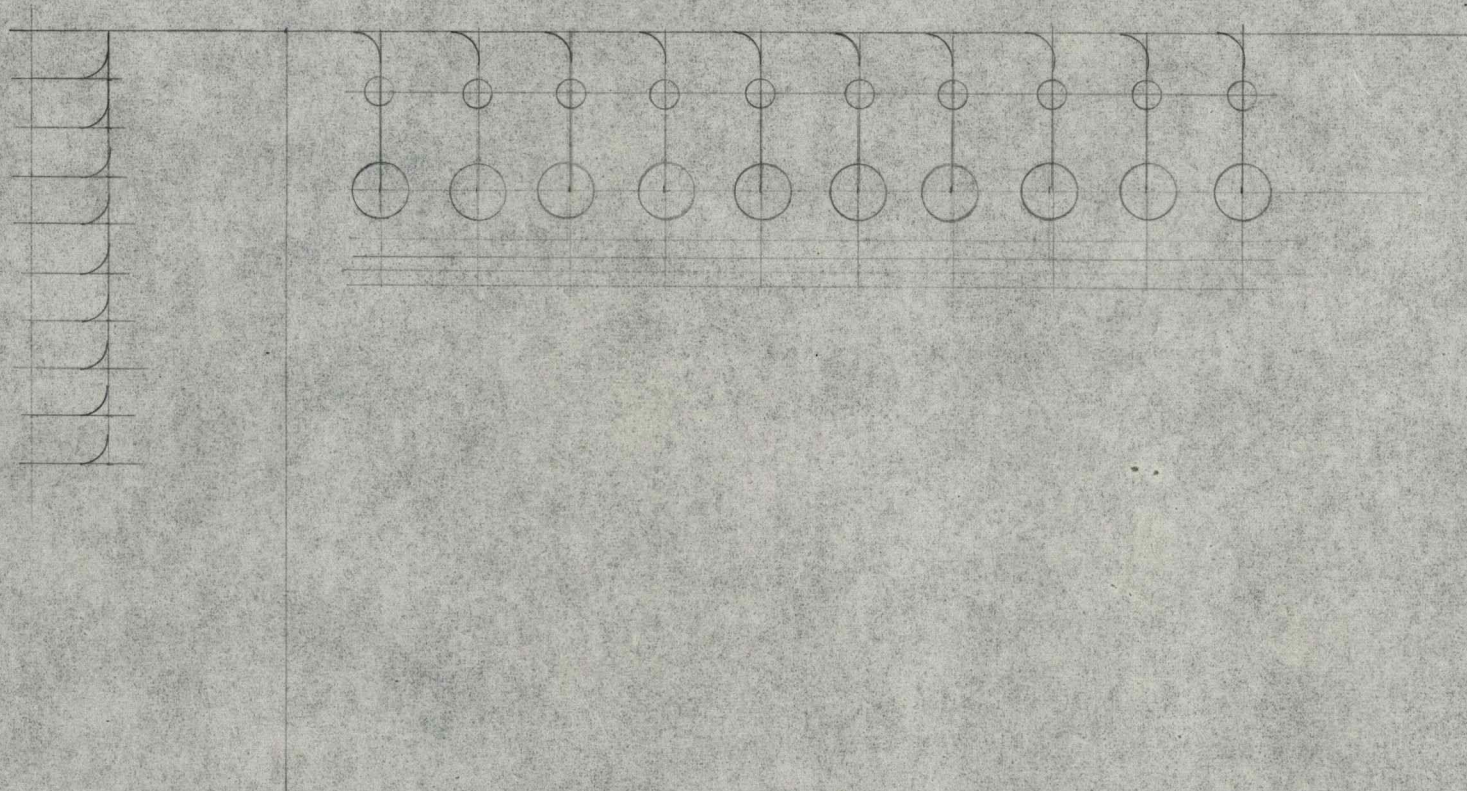
CHANNEL

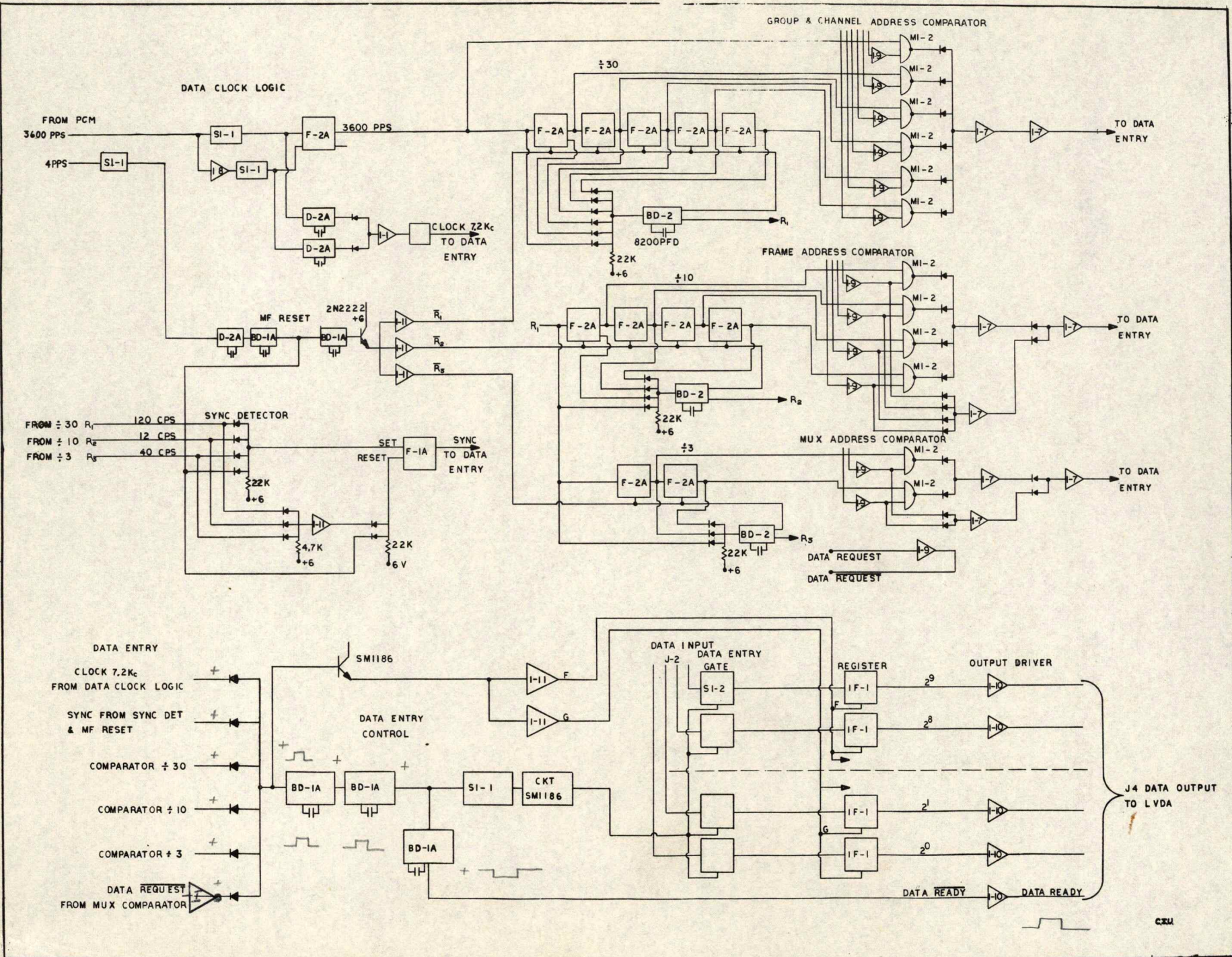


FRAME



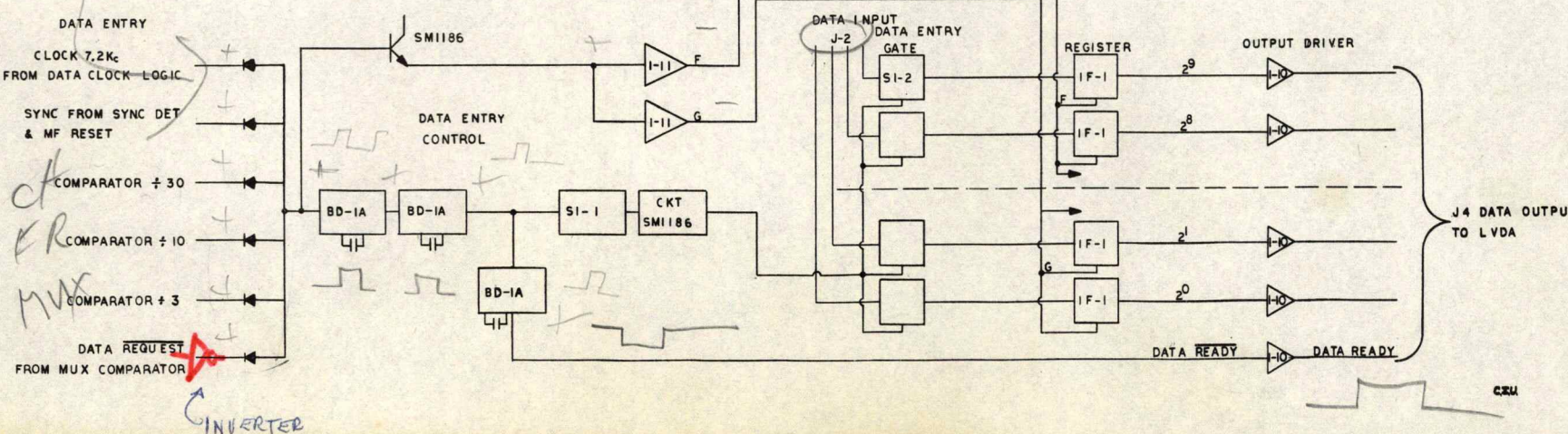
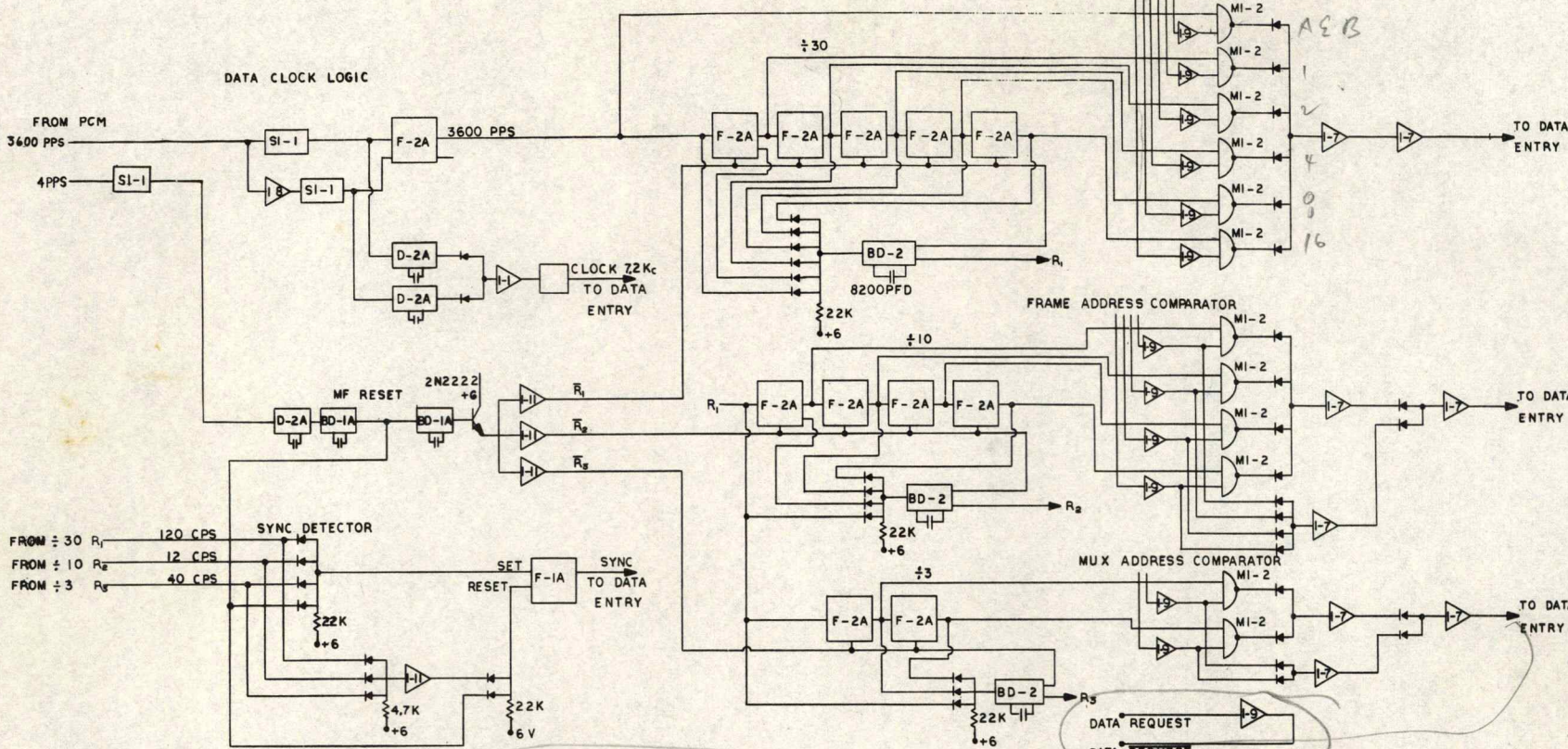






160K21

GROUP & CHANNEL ADDRESS COMPARATOR

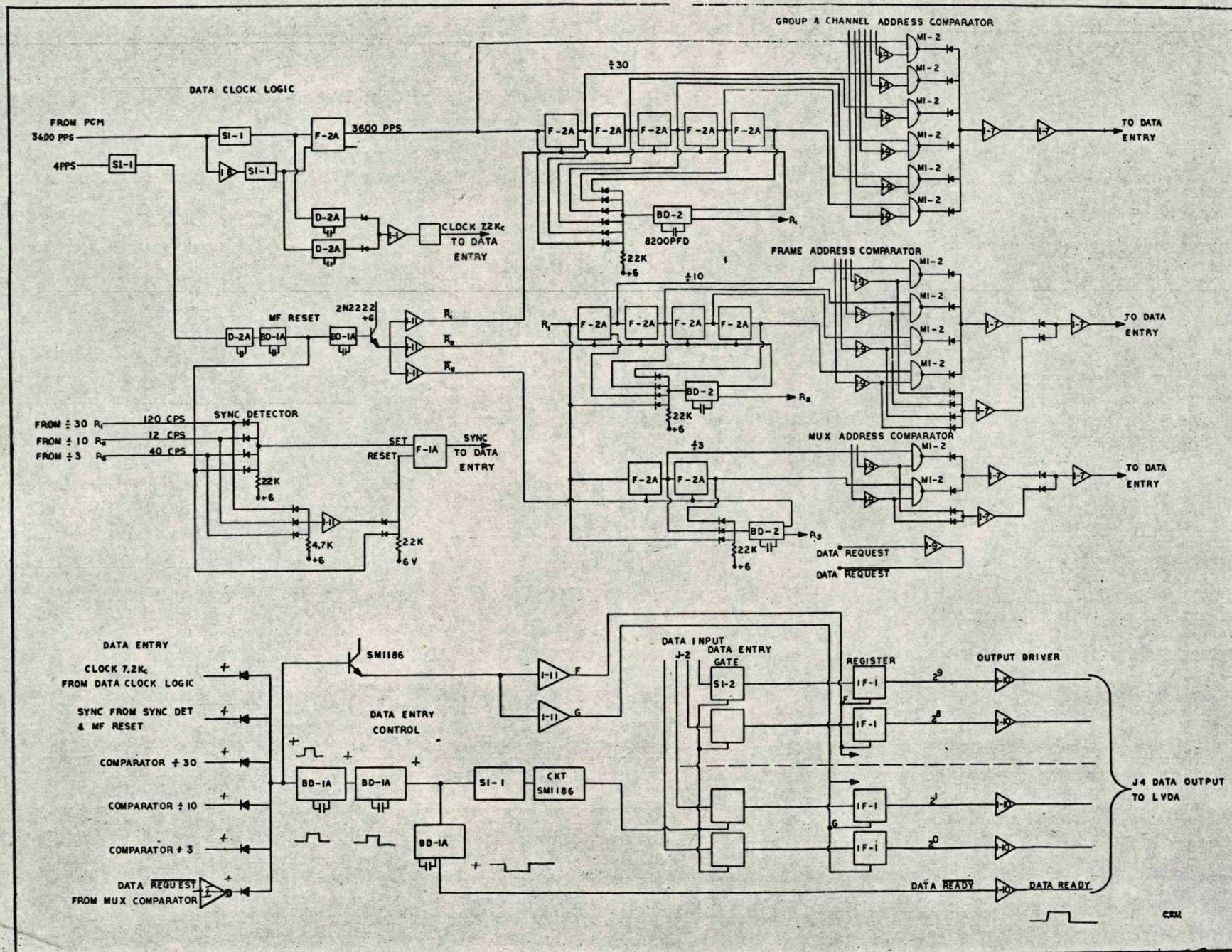


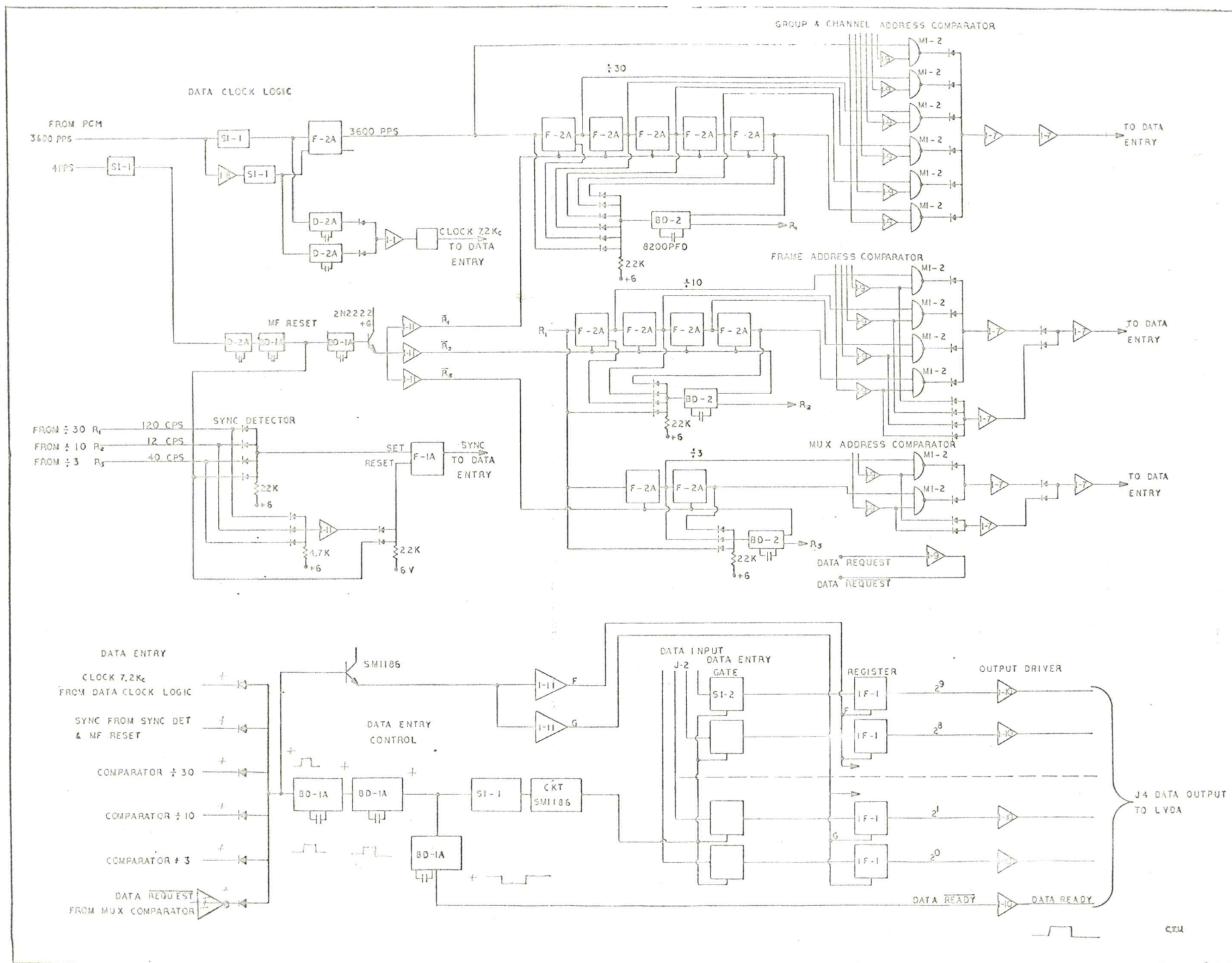
XERO COPY

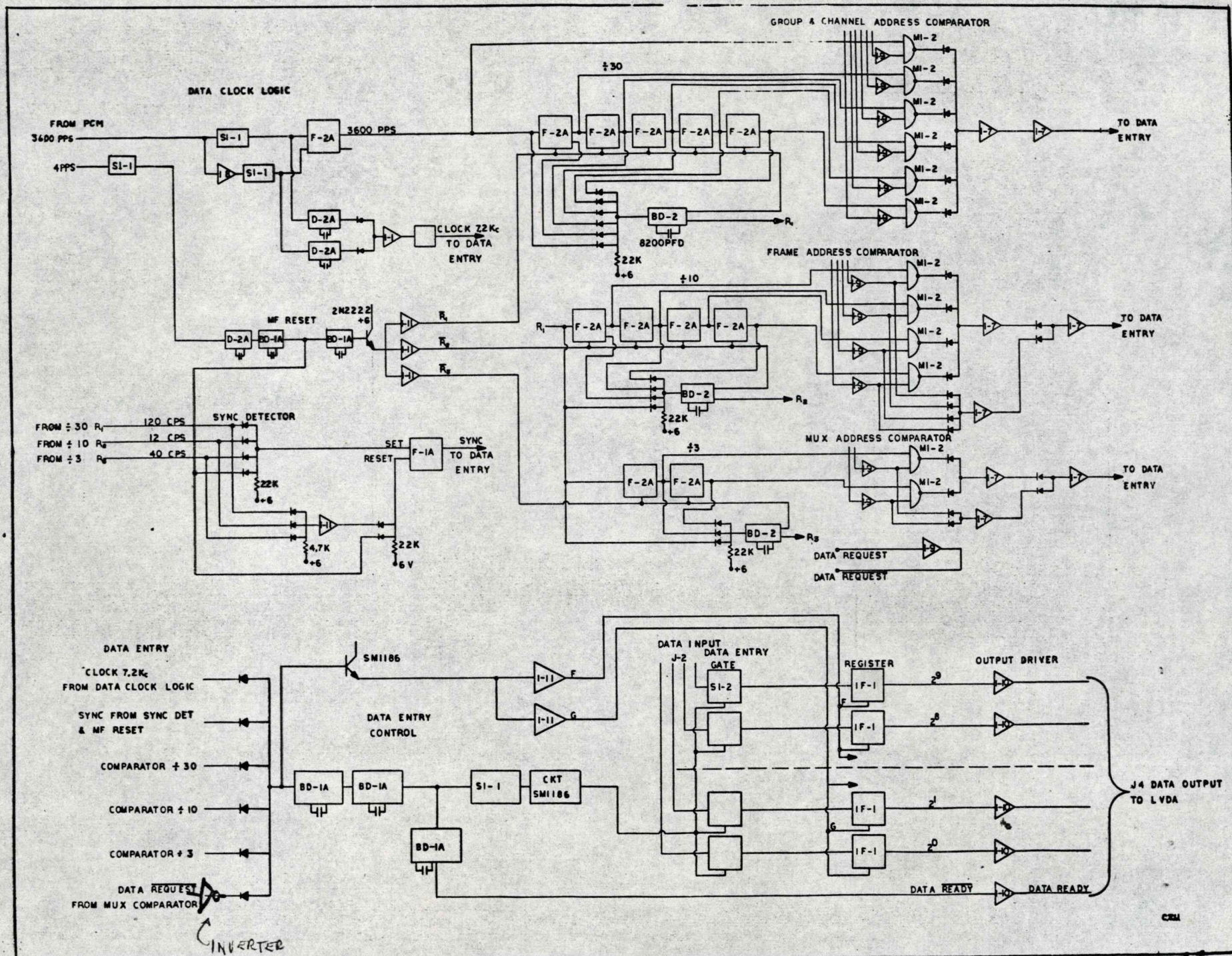
XERO COPY

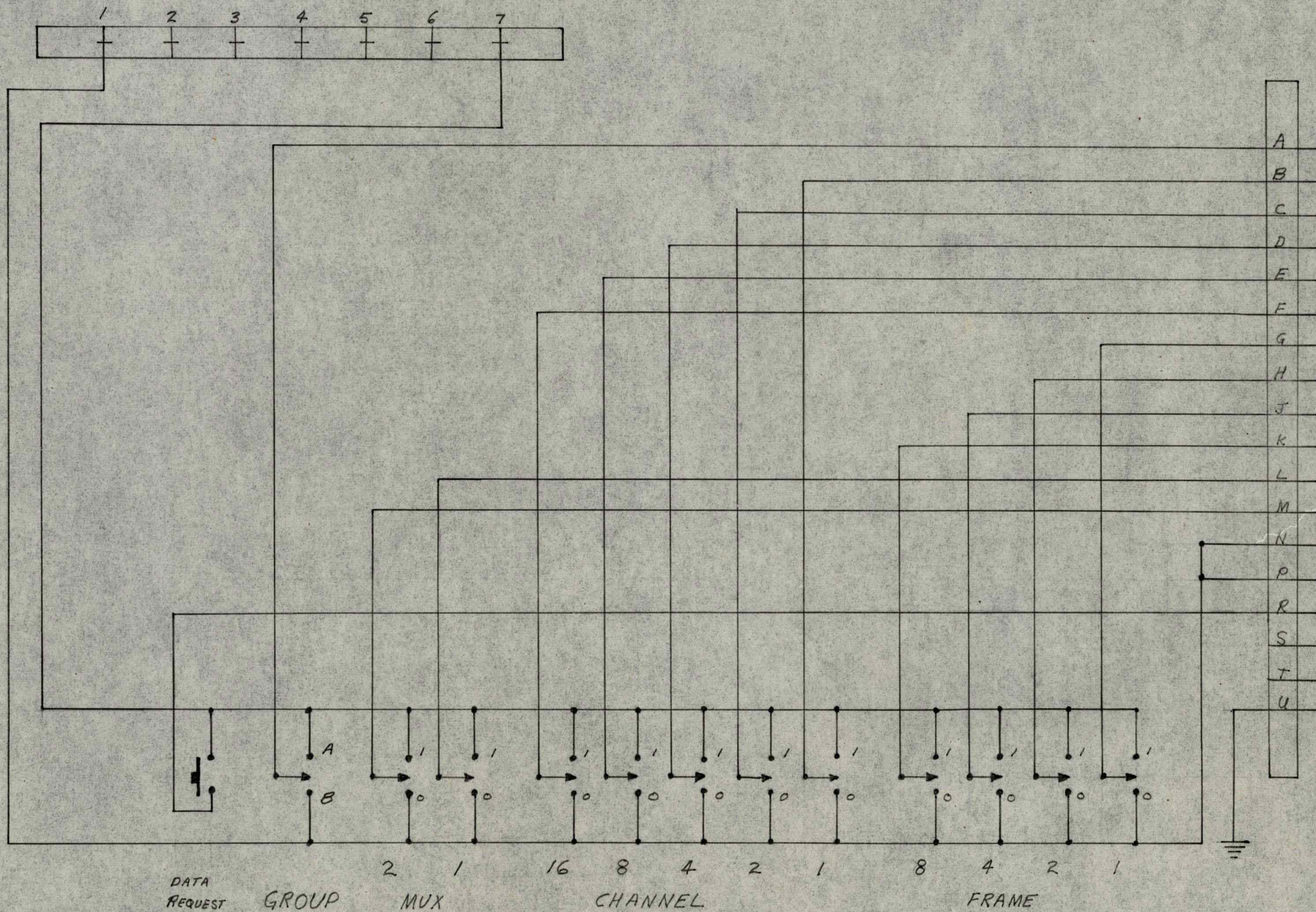
XERO COPY

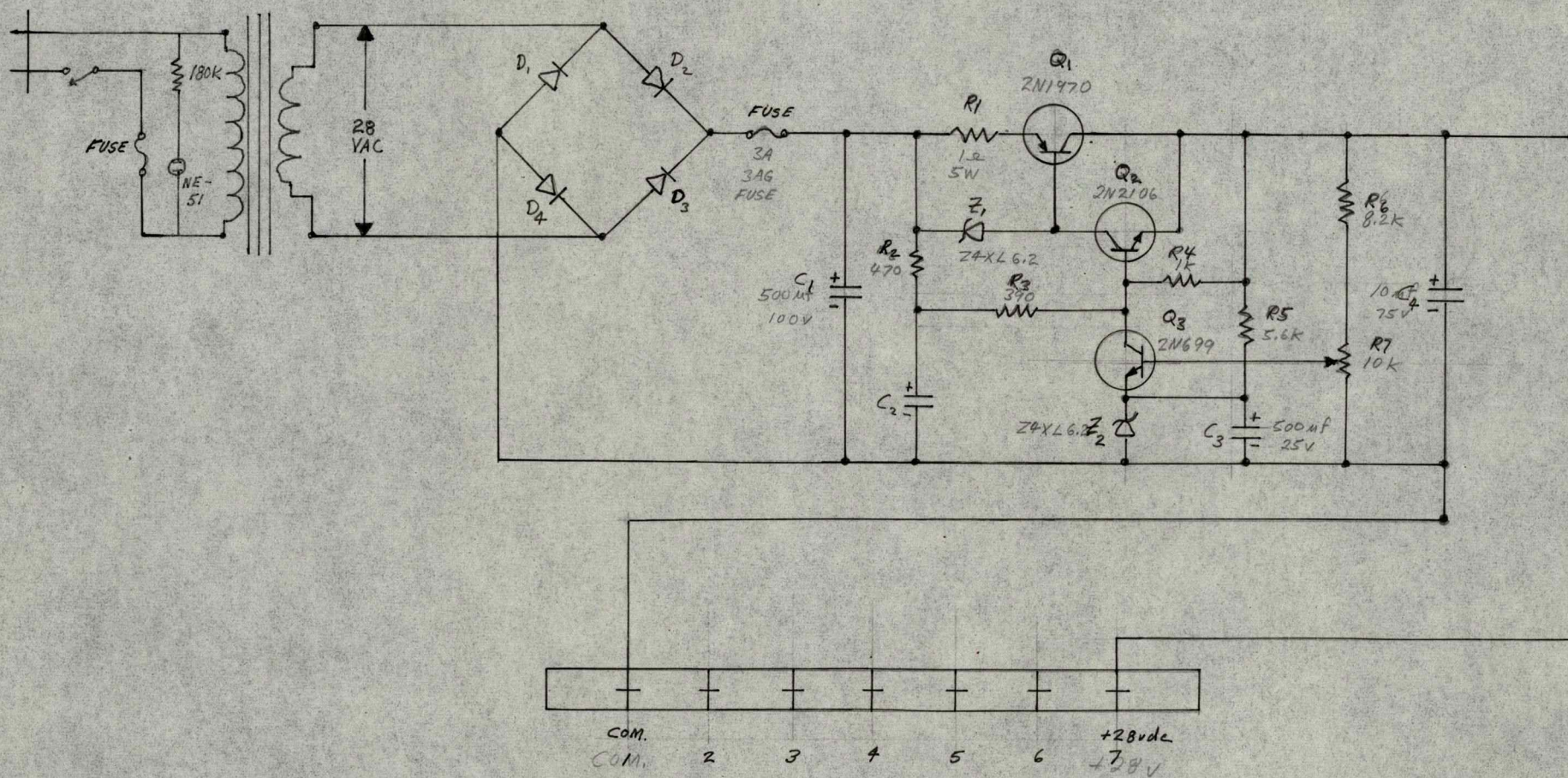
XERO COPY

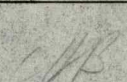








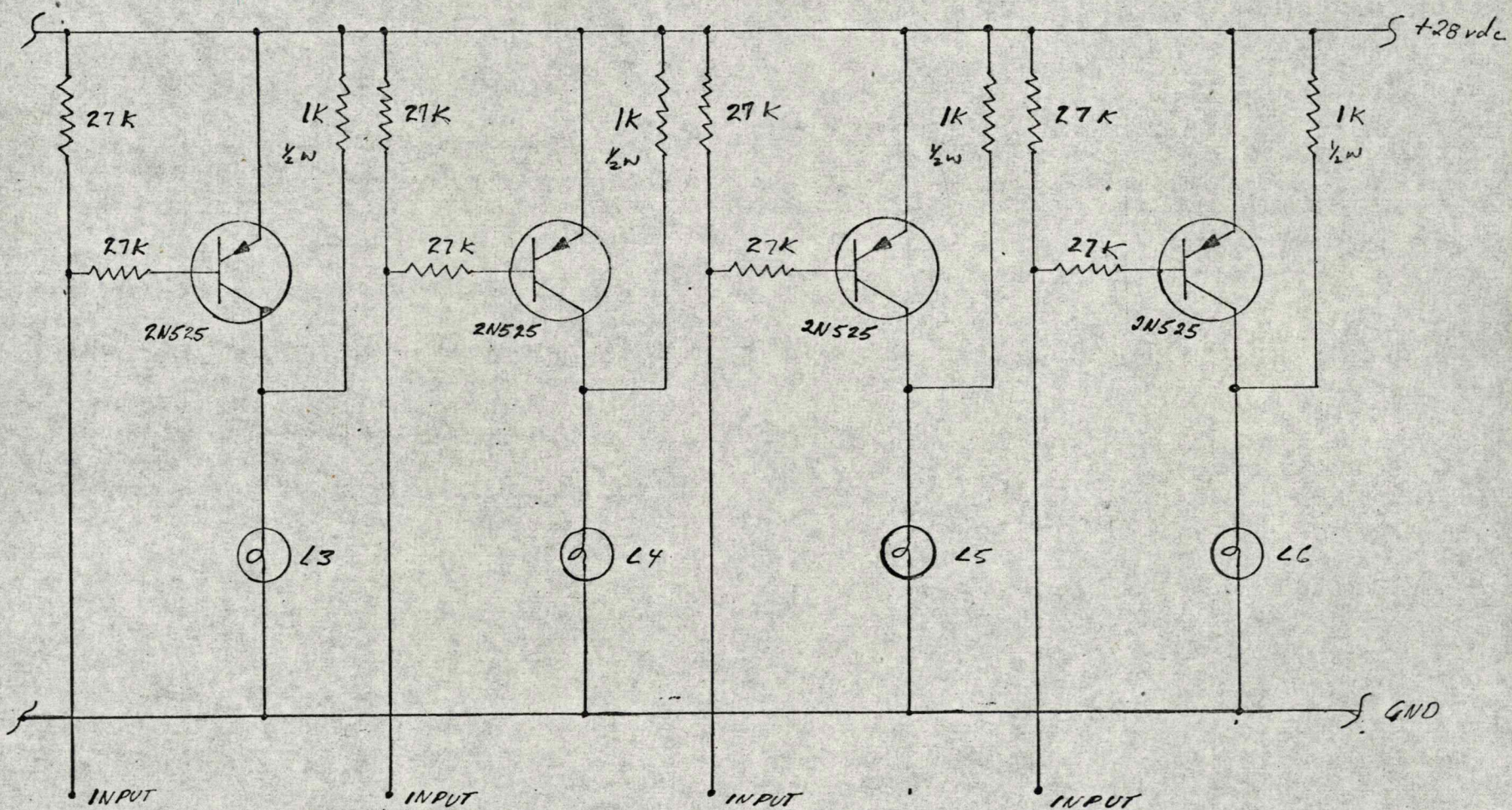


	Sheet No. 1
	COMPUTER INTERFACE UNIT C/O BOX POWER SUPPLY

CIU CHECKOUT BOX (501)

LAMP DRIVER CIRCUITS (2⁸ - 2⁹)

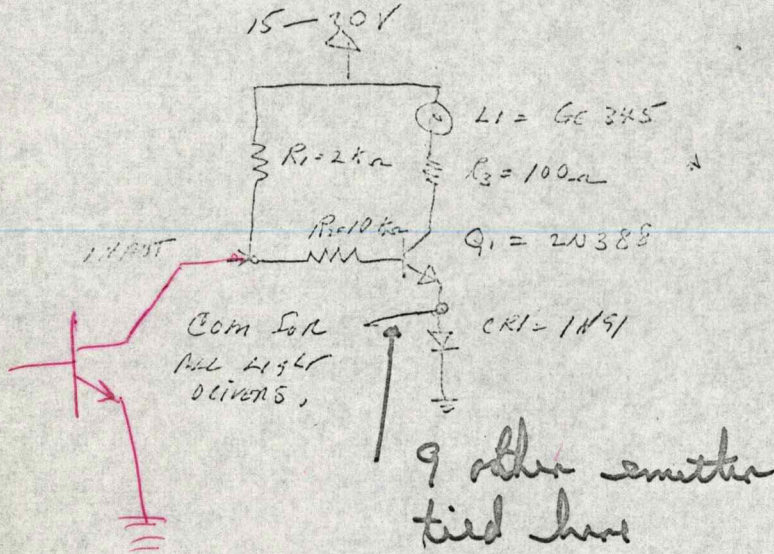
CHB



ALL RESISTORS $\frac{1}{4}W$ UNLESS OTHERWISE SPECIFIED

C1U Panel for IU

C1U TEST BOX - INDICATIONS



INPUTS	BIT	PIN
2 ⁹	-	A
(output of C1U)	2 ⁸	B
	2 ⁷	C
	2 ⁶	D
	2 ⁵	E
	2 ⁴	F
	2 ³	G
	2 ²	H
	2 ¹	J
	2 ⁰	K

DATA IN COM - L

DATA REQ - M

(N should interface L)

ADDRESSING

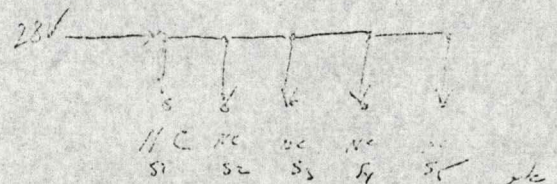
ADDRESS BIT #1 (3600 PPS) =	A
" " 2 ($\div 30$) =	B
" " 3 ($\div 30$) =	C
" " 4 ($\div 30$) =	D
" " 5 ($\div 30$) =	E
" " 6 ($\div 30$) =	F
" " 7 ($\div 10$) =	G
" " 8 ($\div 10$) =	H
" " 9 ($\div 10$) =	J
" " 10 ($\div 10$) =	K

ADDRESS BIT #12 = M ($\div 3$)

ADD COM - ~~10~~ (should be 11)

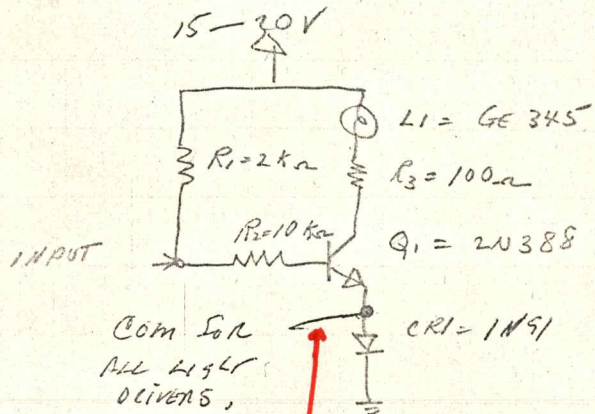
DATA REQ - R

PART OF	A	B	C	D	E
JS	1/6	1/6	1/6	1/6	1/6



CIU Panel for IU

CIU TEST BOX - INDICATORS



9 other emitter tied here

INPUTS -	BIT	PIN
(output of CIU)	2^9	A
	2^8	B
	2^7	C
	2^6	D
	2^5	E
	2^4	F
	2^3	G
	2^2	H
	2^1	J
	2^0	K

DATA IN Com - L

ADDRESSING

DATA READY - M

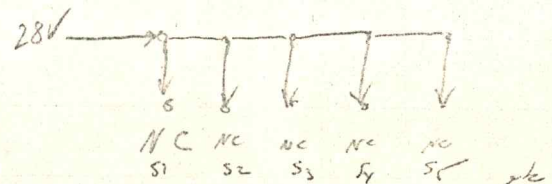
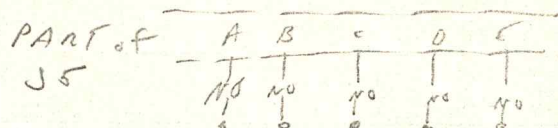
(N shorted internally to L)

ADDRESS BIT #1 (3600 PPS) =	A
" #2 ($\div 30$) =	B
" #3 ($\div 30$) =	C
" #4 ($\div 30$) =	D
" #5 ($\div 30$) =	E
" #6 ($\div 30$) =	F
" #7 ($\div 10$) =	G
" #8 ($\div 10$) =	H
" #9 ($\div 10$) =	J
" #10 ($\div 10$) =	K
" #11 ($\div 3$) =	L

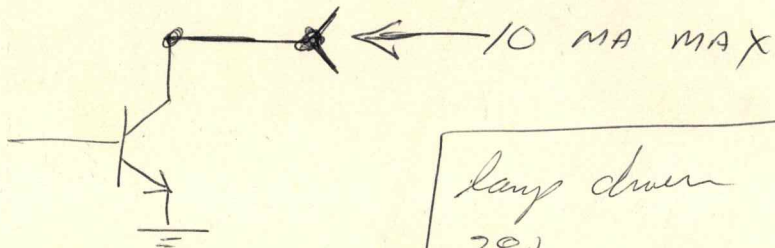
ADDRESS BIT #12 = M ($\div 3$)

ADD Com - N (shorted to N)

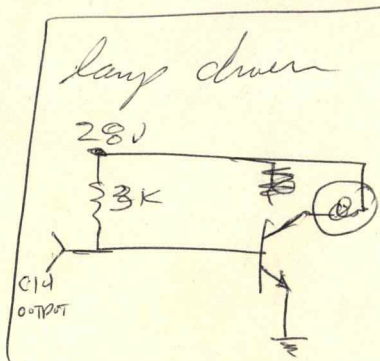
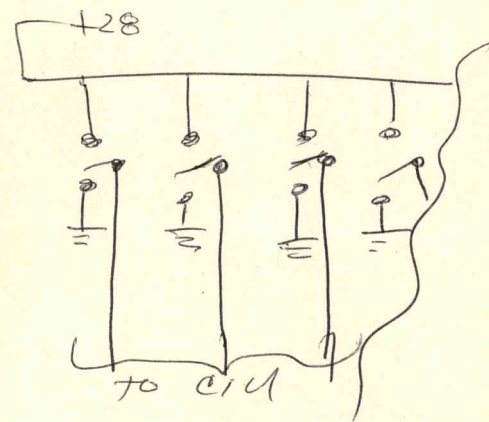
DATA REQ - R



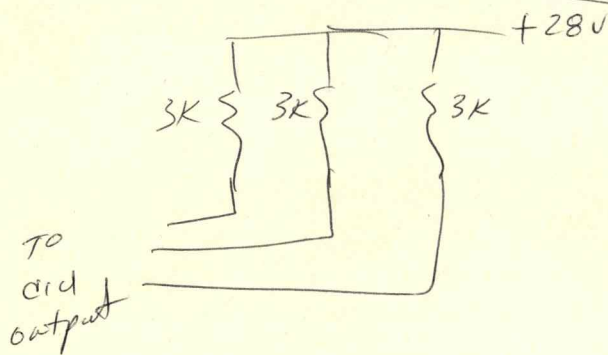
Output of C14



address control



Load ch



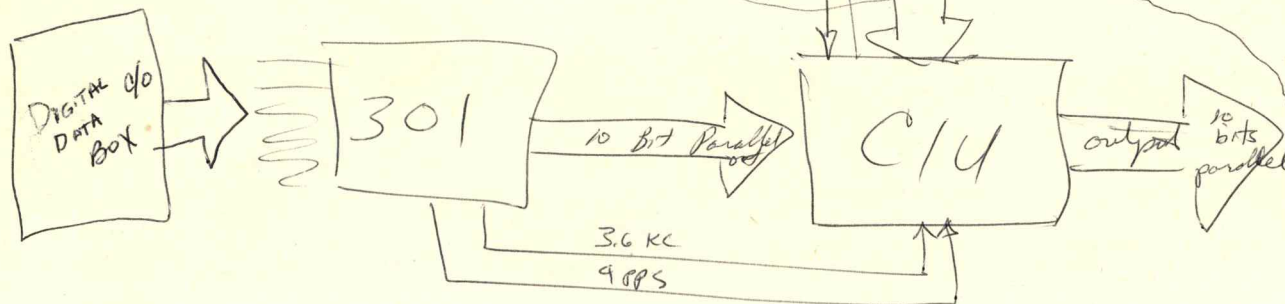
10 outputs & 1 data ready

C14 I/O Box

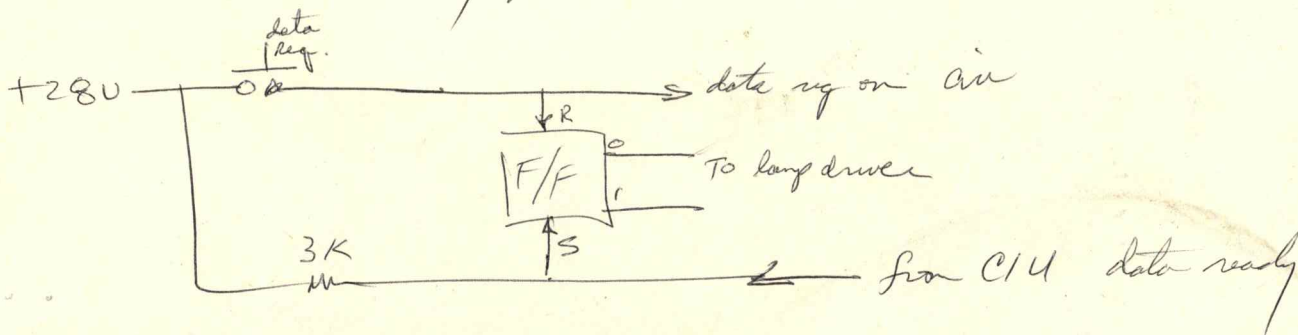
data ready (upto 250 msec after data req)

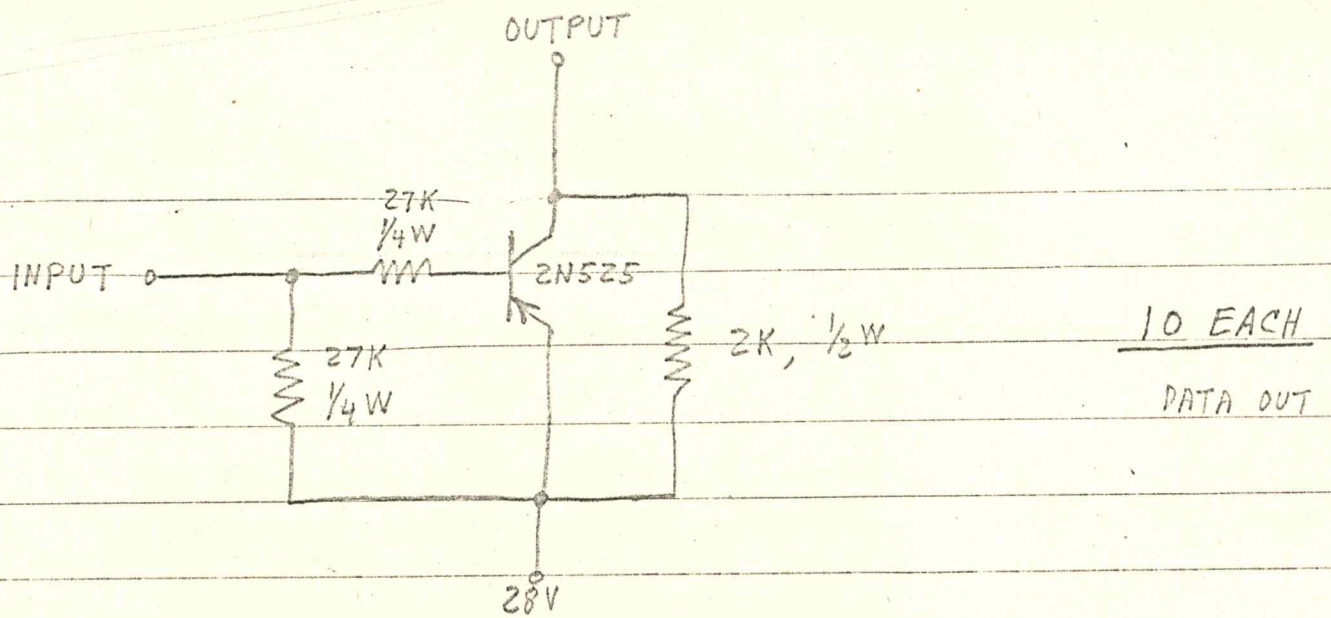
data request

14 lines address

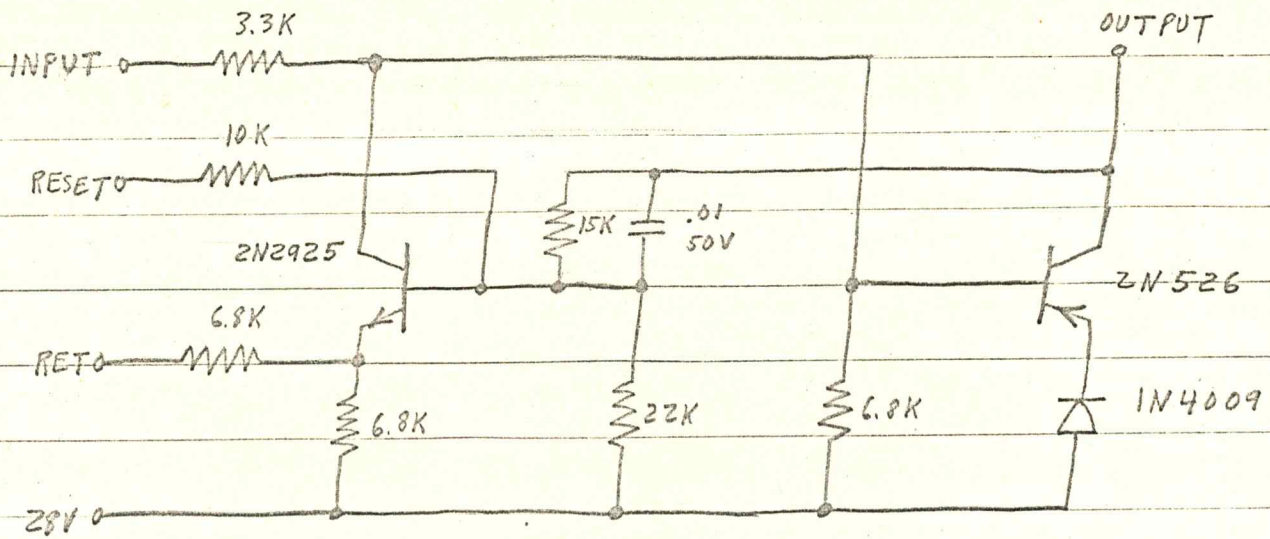


to read data ready pulse





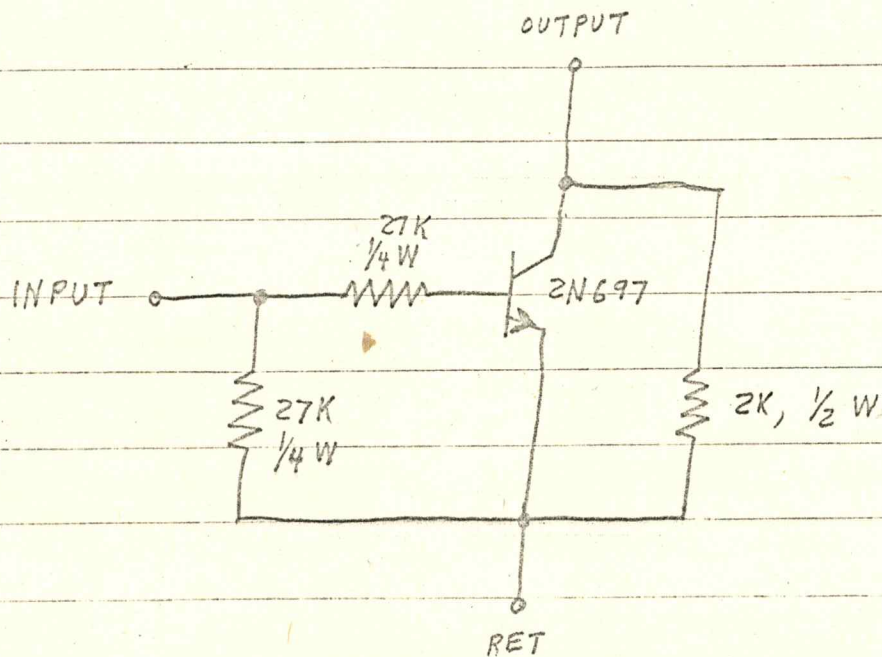
10 EACH
DATA OUT



ALL $\frac{1}{4}$ WATT RESISTORS

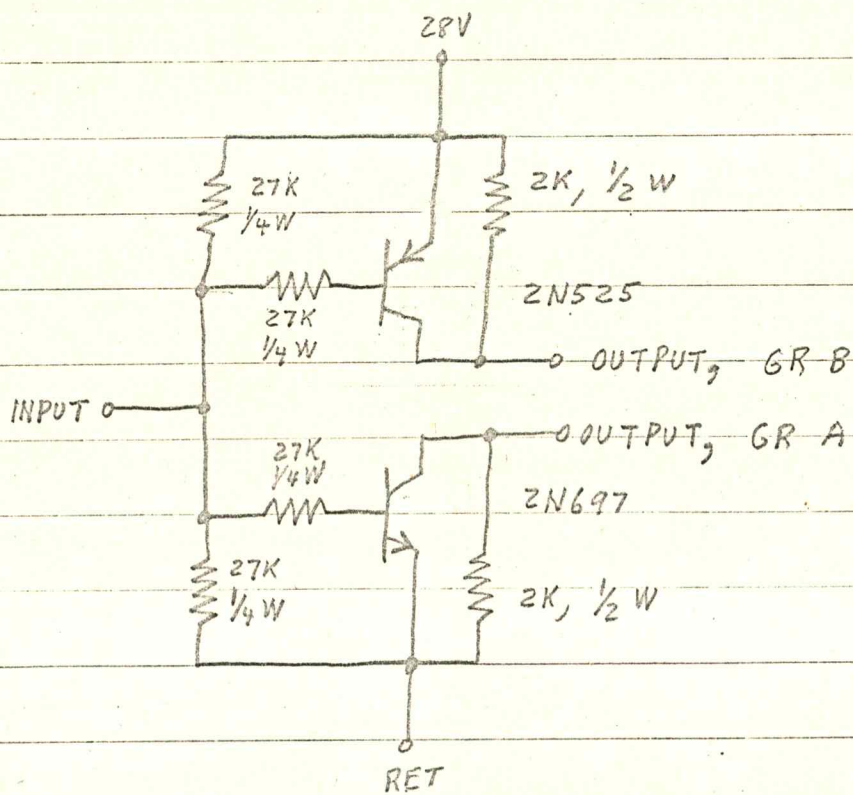
1 EACH

DATA REQUEST



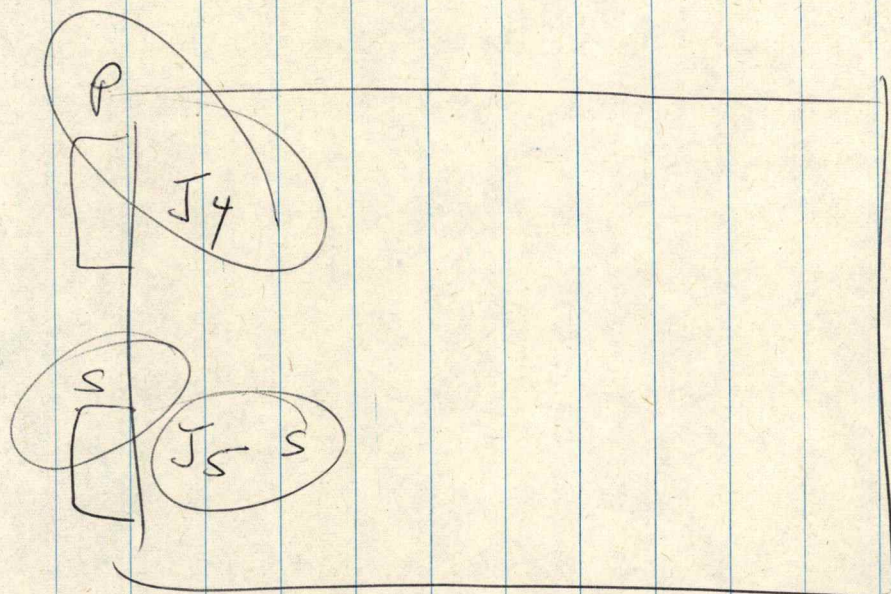
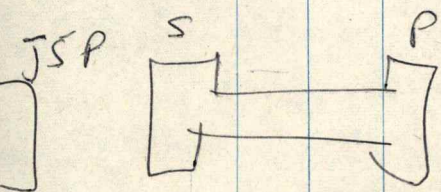
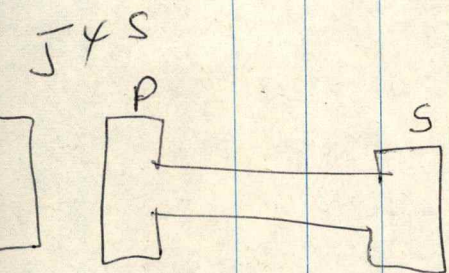
11 EACH

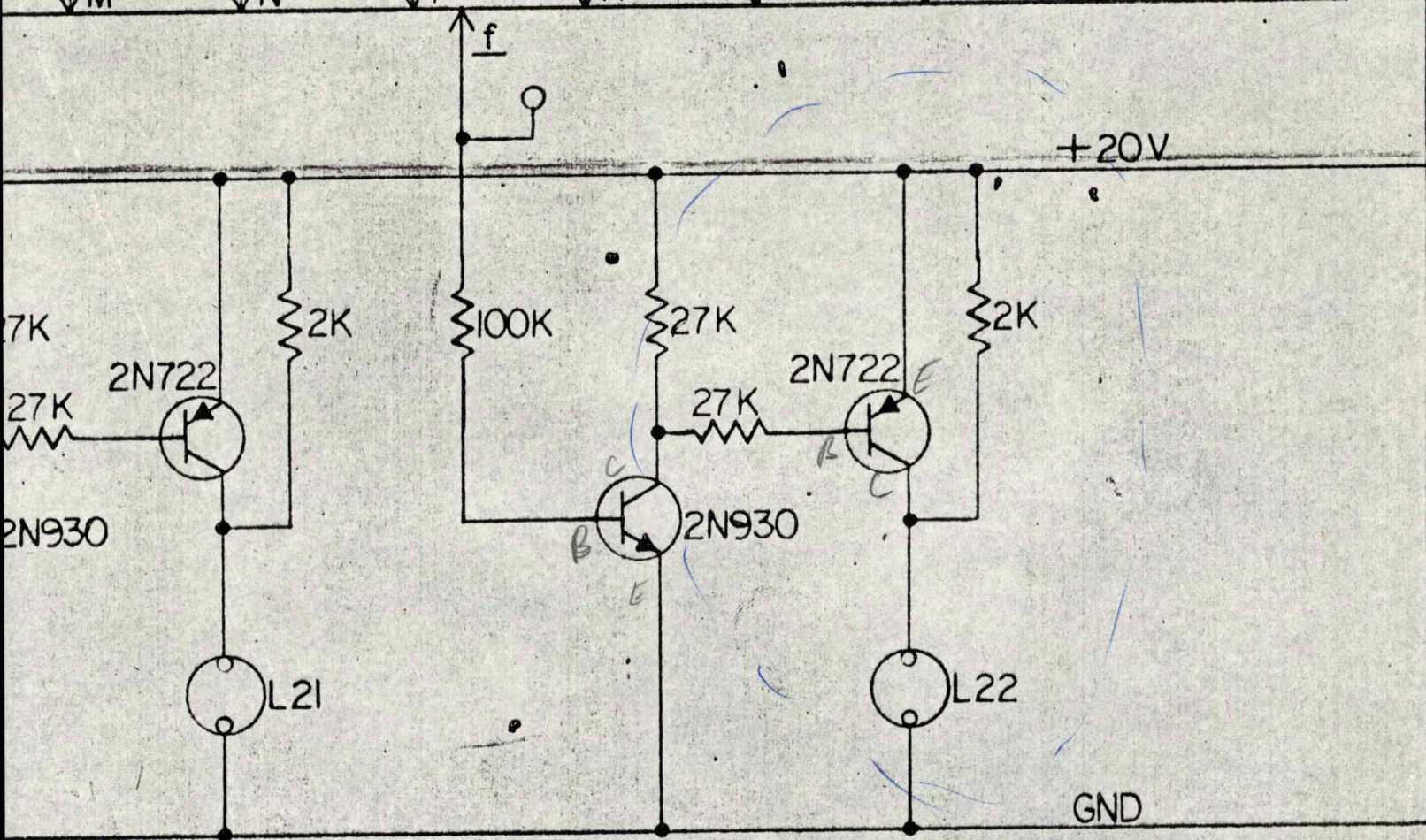
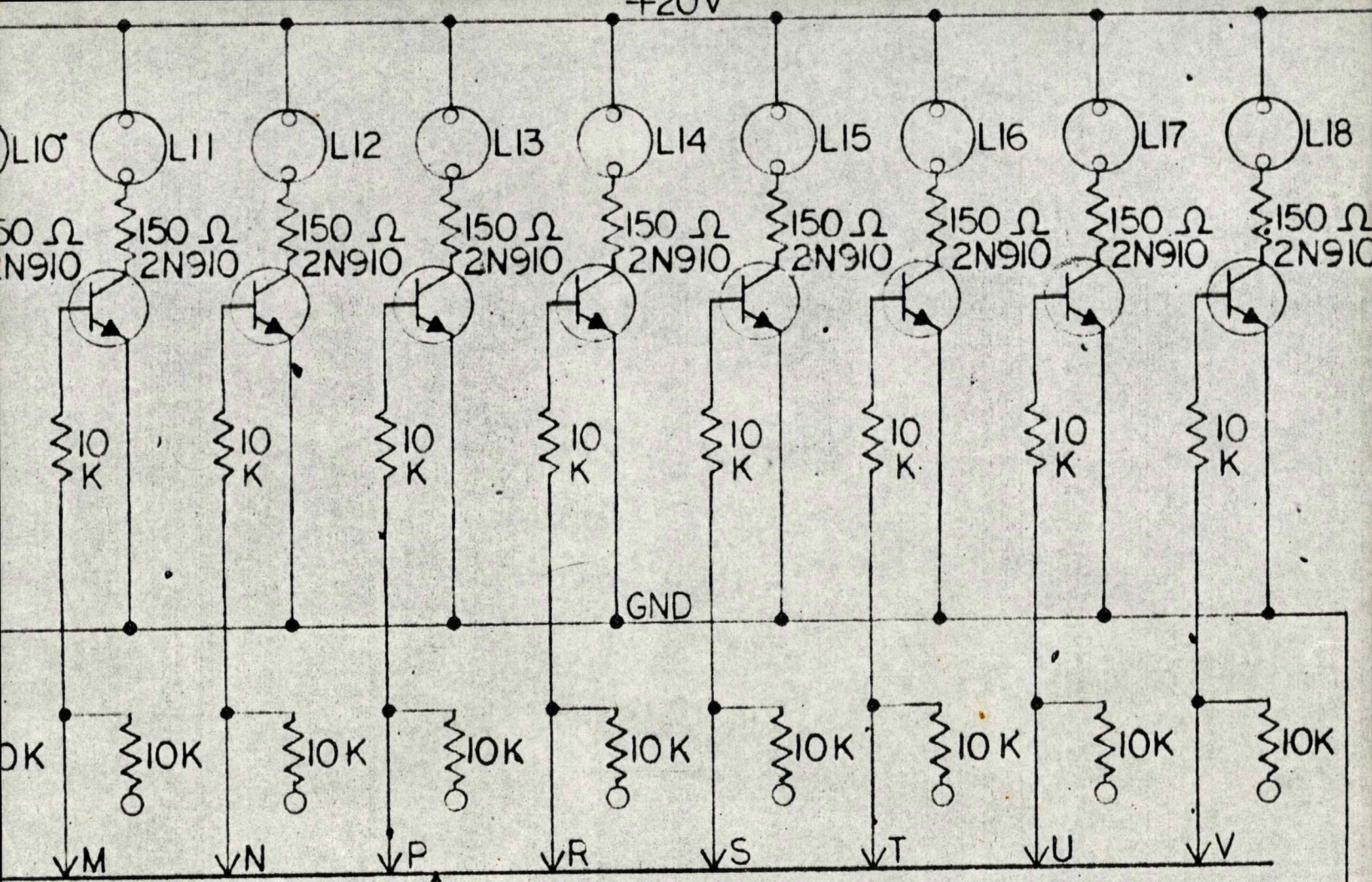
ADDRESS



1 EACH

GROUP





C.I.U. SKETCH
LVO-132

J5

ADD INPUT FR LVDA

12 LINES

ALL I-9

1 DATA REQUEST

I-9

**FOR
INFO
ONLY**

1 1-6: CA 1 - GROUP comp

2 CA ϕ CH1

3 1 CH2

4 2 CH3

5 3 CH4

6 4 CH5

} Chan comp.

7 FA ϕ

8 FA1

9 FA2

10 FA3

} FA comp.

11 MA ϕ

12 MA1

} Mux comp.

J4 OUT TO LVDA I-10

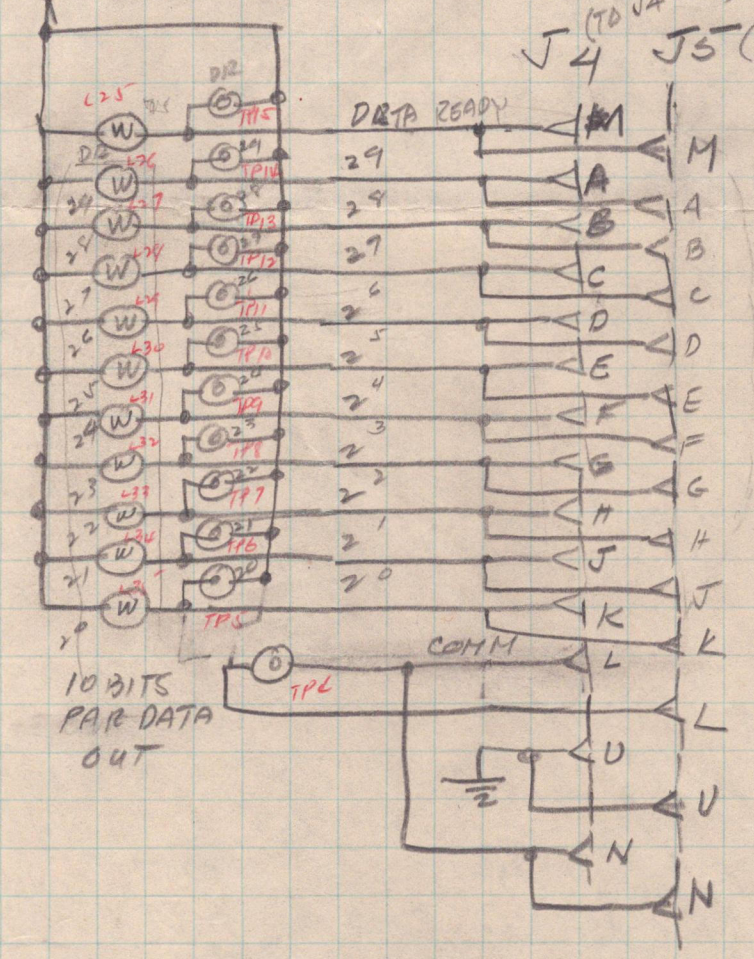
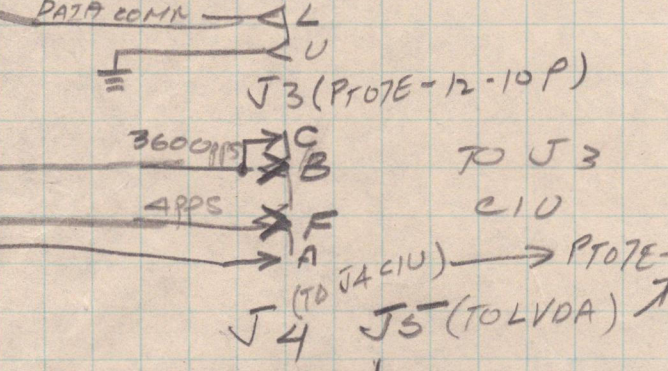
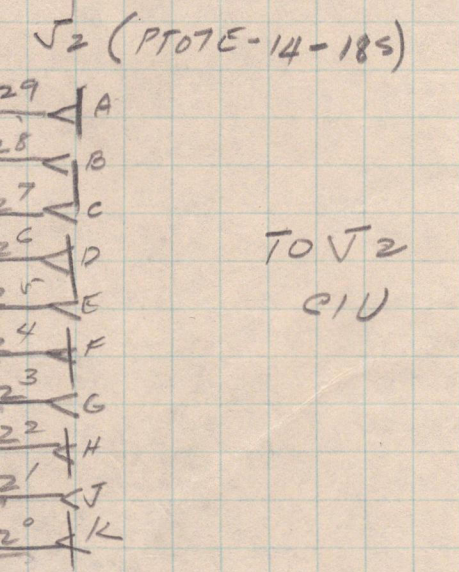
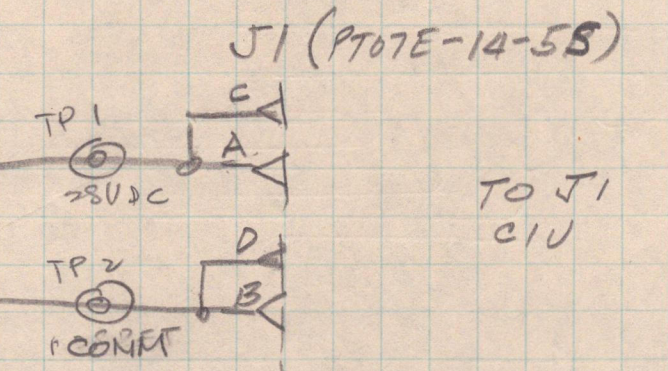
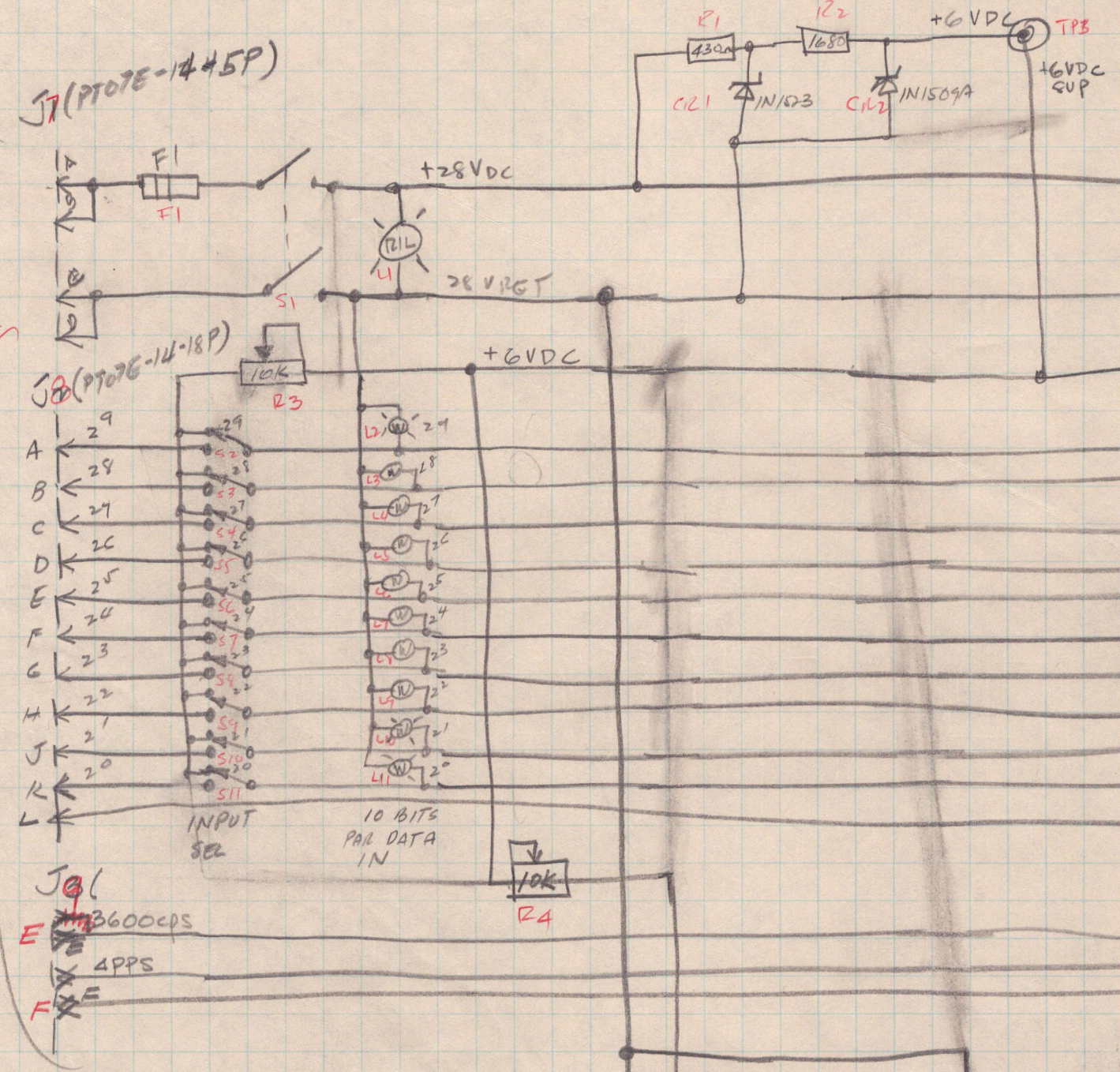
2⁰, 2¹, 2², 2³, 2⁴, 2⁵, 2⁶, 2⁷, 2⁸, 2⁹, DR 

J2 PAR FR 301

J3 3600 PPS & 4 PPS syn

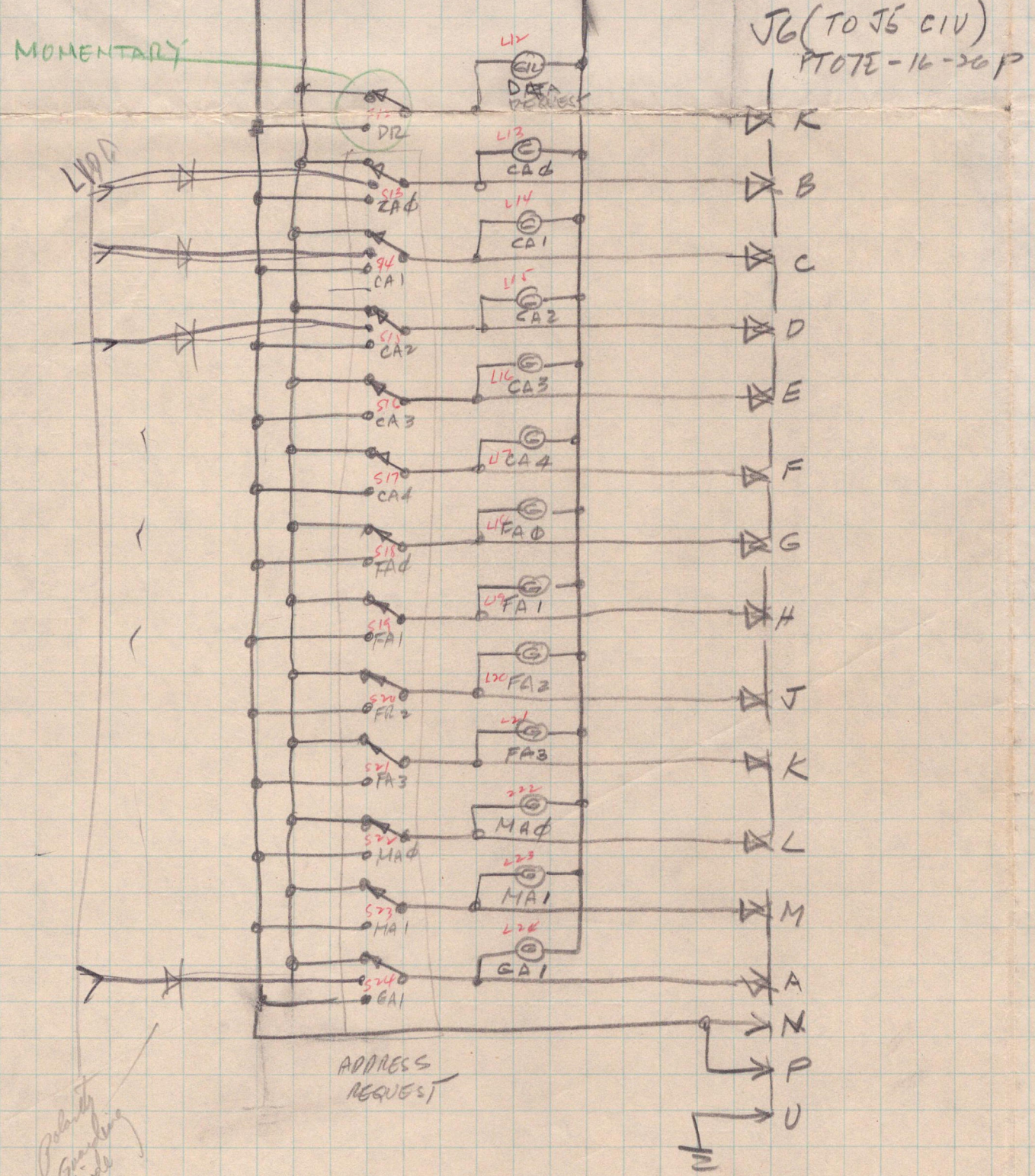
J7 Power

FROM 801 C/O BOX



- J1 (PT07E-14-5B)
- J2 (PT07E-14-18S)
- J3 (PT07E-12-10P)
- J4 (PT07E-14-18S)
- J5 (PT07E-14-18S)
- J6 (PT07E-16-26P)
- J7 (PT07E-14-5P)
- J8 (PT07E-14-18P)
- J9 (PT07E-18-32B)

- P1 ←
- P2 ←
- P3 ✗
- P4 ←
- P6 ✗
- P7 ✗
- P8 ✗
- P9 ✗

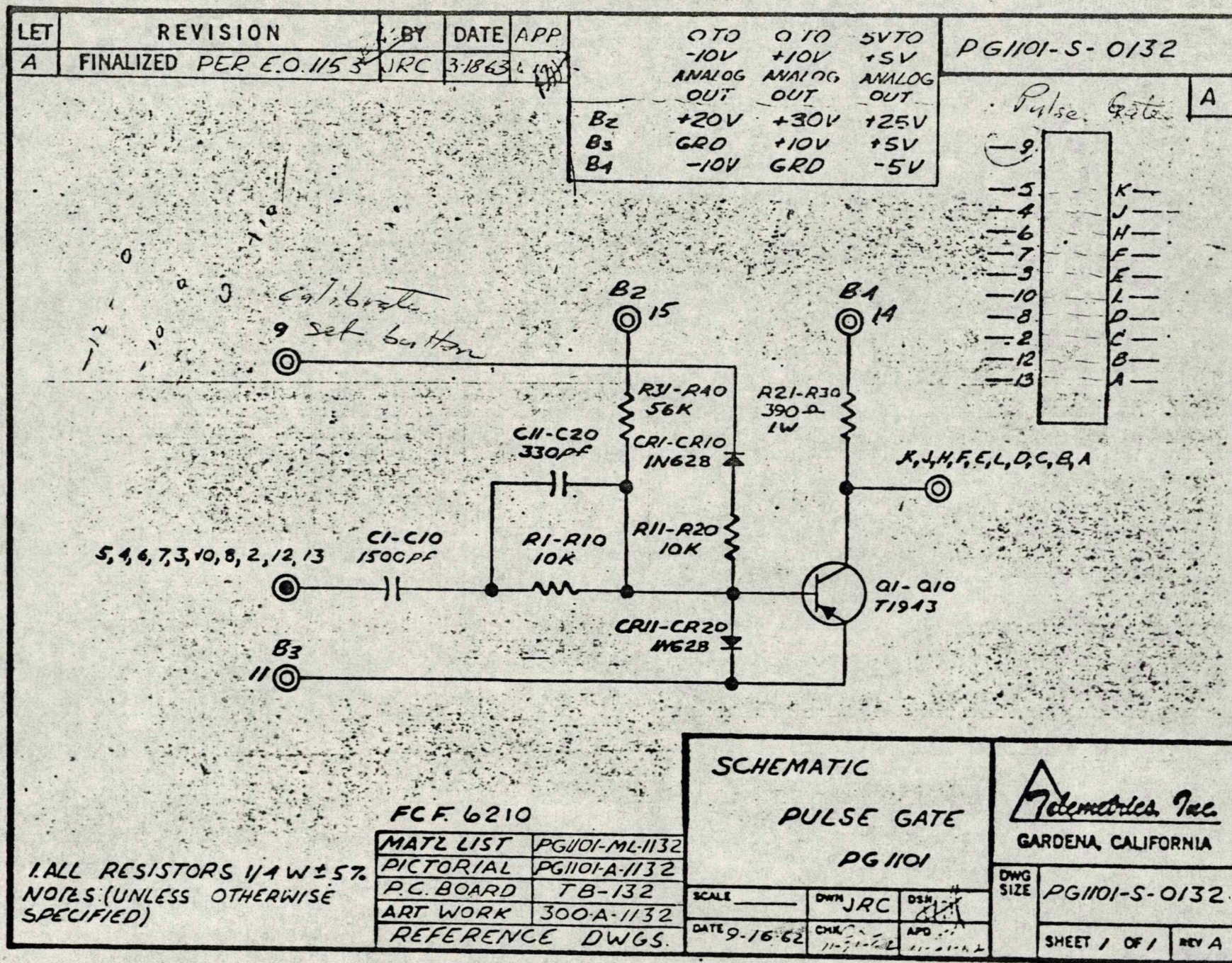


C.I.U C/O PANEL
RACK MTD
(FOR IBM ONLY)

Lamp Drivers

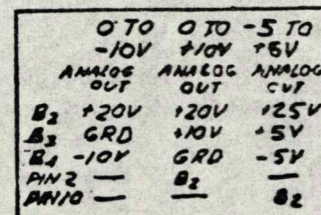
Figure 7. Pulse Gate PG-1101, Schematic Diagram

FOR TRAINING ONLY



PA 232-S-034

Б



PA. 232

OWN JRC

CHLO 41

11-181

11-14-62

Recorded _____
Date 11-11-11 By _____

PA 232-S-034

SHEET / OF /	REV 8
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FCF:DS56/03

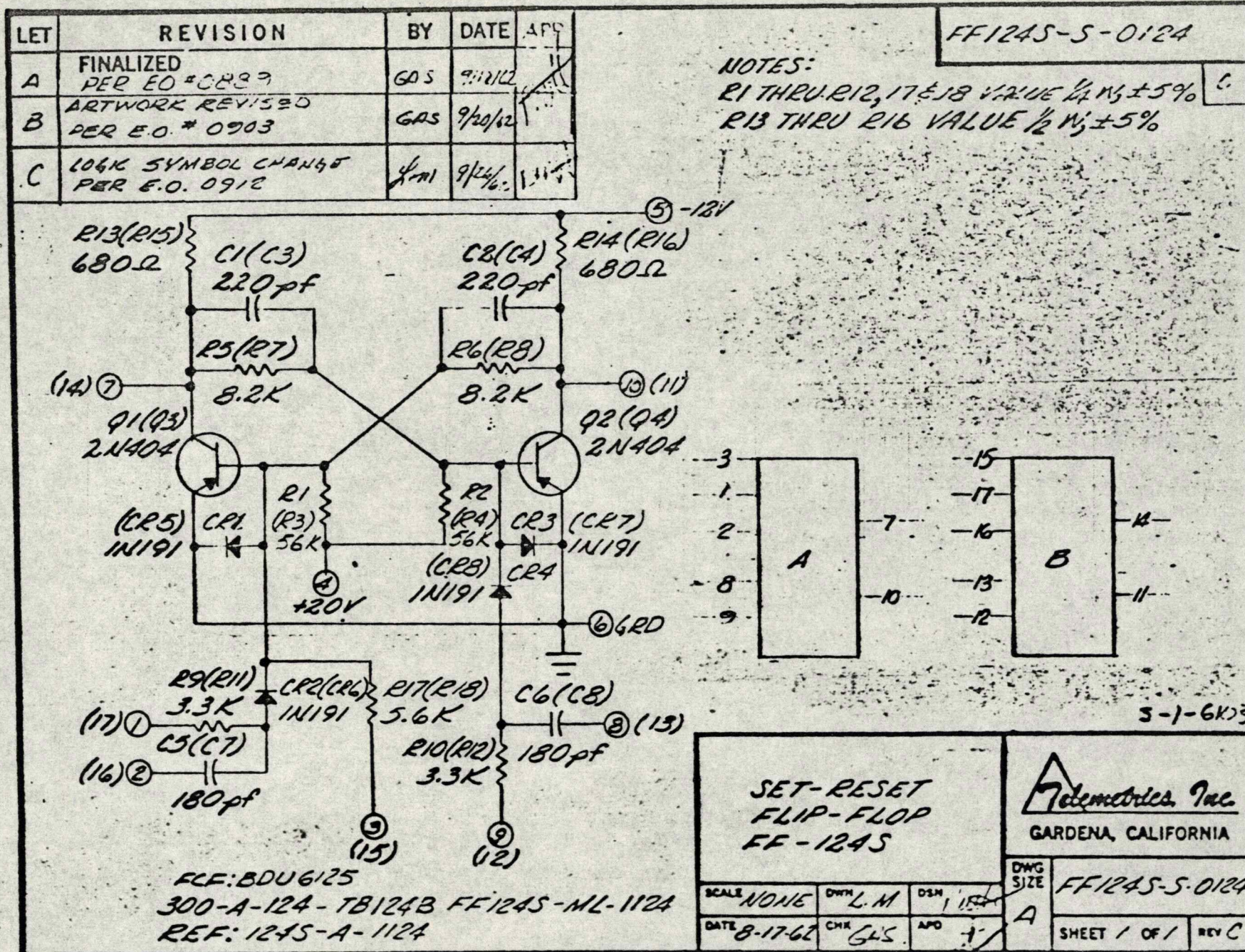
MAT'L LIST	PA 232-MK-134
PICTORIAL	PA 232-A-134
P.C. BOARD.	TB-34A
ARTWORK	300-A-134
REFERENCE	DWGS.

Figure 9. Power Amplifier PA-232, Schematic Diagram

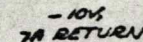
FOR TRAINING ONLY

Figure 10. Set-Reset Flip-Flop FF-124S, Schematic Diagram.

FOR TRAINING ONLY



ALL RESISTORS 1/2 WATT, 5%.
NOTES: UNLESS OTHERWISE SPECIFIED.



- NOV. 7A

⑬ POS SENSE

④ MED. SENSE

REVISIONS			
DATE	DESCRIPTION	DATE	APPROVED
1-1-68	C4 was 17300 MF. C3 was 4000 WF.	1-1-68	W.S.

REV	SYM	CODE IDENT	PART OR IDENTIFYING NO.	DESCRIPTION OR DESCRIPTION	MATERIAL	SPECIFICATION	ITEM NO.
LIST OF MATERIAL							
UNLESS OTHERWISE SPECIFIED				APPROVALS	DATE	PIZ POWER INSTRUMENTS CORPORATION 10000 CALIFORNIA	
DIMENSIONS ARE IN INCHES				DRAFTSMAN	11/10/69	SCHEMATIC, PS1440	
REMOVE ALL SHARP & SHARP EDGES				DESIGNER			
THREADS ARE CLASS 2				CHANGER			
DIMENSIONAL TOLERANCES				PROOF			
SHEET METAL - MACHINE				RELEASE			
ANGLES: 1" = 2"							
SS 0.030 2.00							
STE 0.010 2.00							
HOLE DIA 0.005 - 0.01							
CONCENTRICITY 0.010							
SURFACE ROUGHNESS PER MIL-STD-199				OTHER ACTIVITY APPROVAL		CODE IDENT NO.	DATE
✓				CONTRACTOR APPROVAL		12069	02621
DO NOT SCALE DRAWING						SCALE	WEIGHT
						SHEET	

627-999