

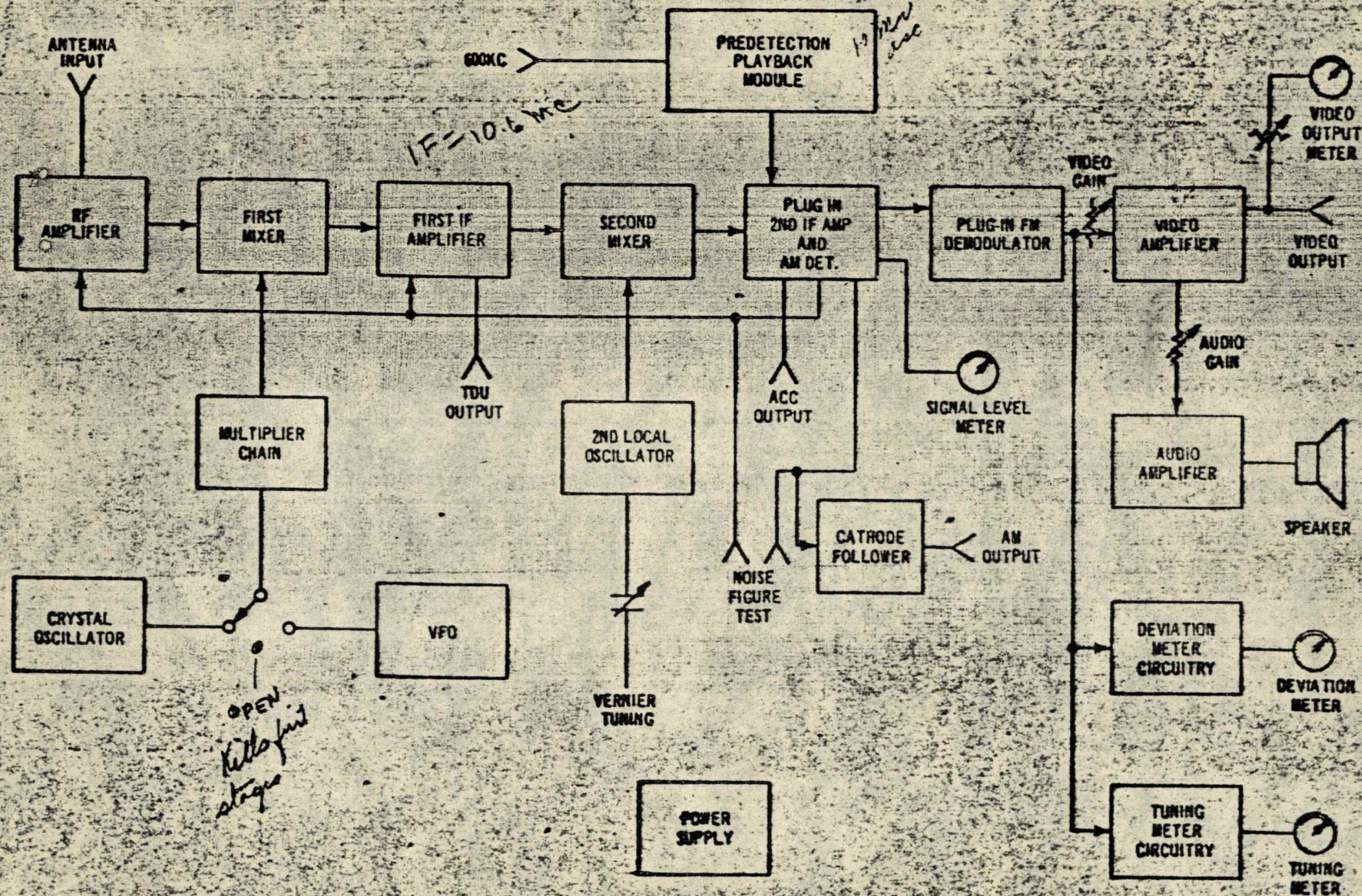


FIGURE I-4. RECEIVER/DEMODULATOR BLOCK DIAGRAM

FOR TRAINING ONLY

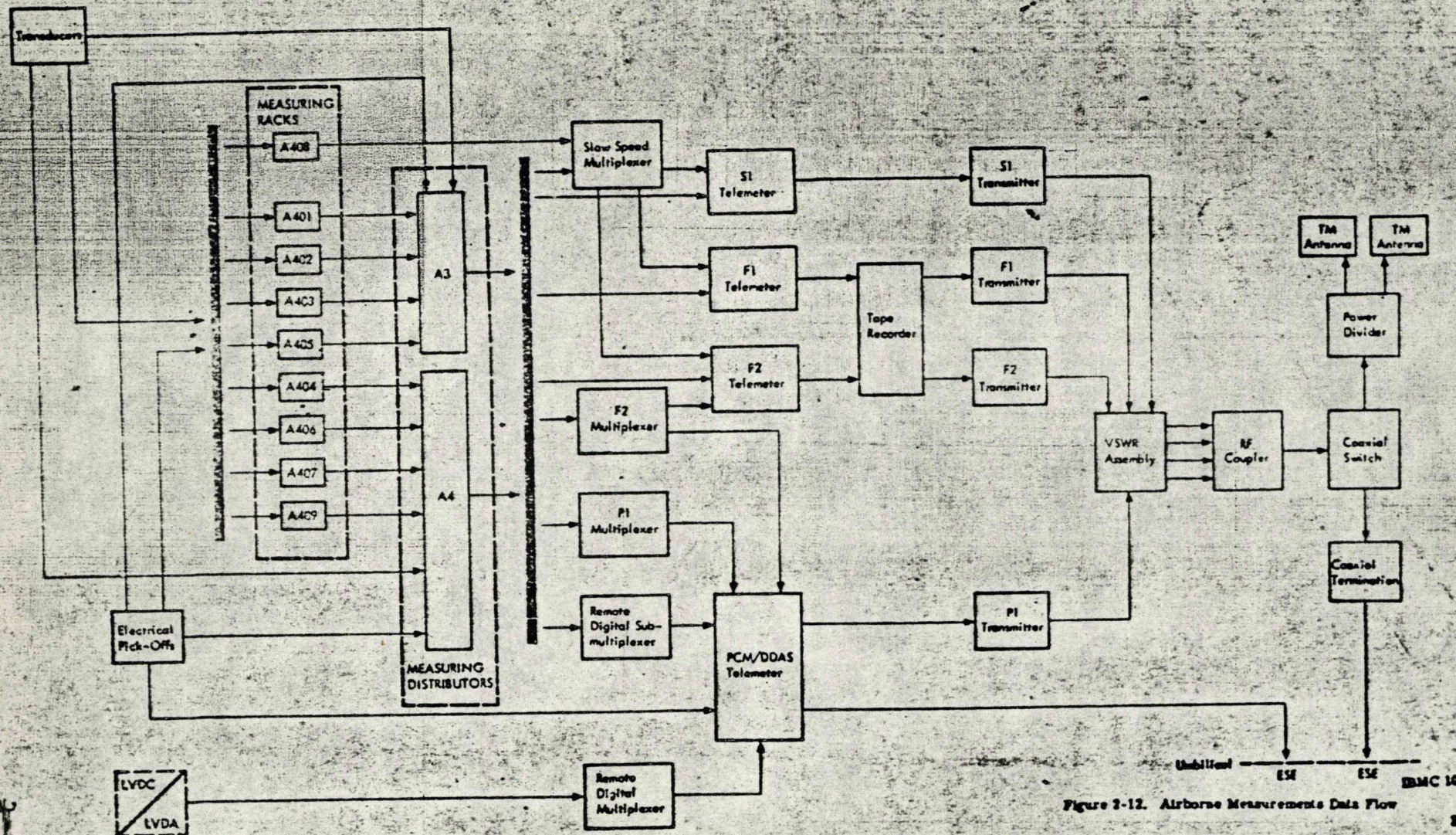


Figure 2-12. Airborne Measurements Data Flow

FOR TRAINING ONLY

POWER SUPPLY

SOURCE SELECTOR PANEL ASSEMBLY

RECEIVER/DEMODULATOR

① DIGITAL SIGNAL SIMULATOR

② DIGITAL SIGNAL SYNCHRONIZER

BLOWER

D - A CONVERTER

D - A CONVERTER

③ QUICK LOOK PANEL ASSEMBLY

DATA SWITCH PANEL ASSEMBLY

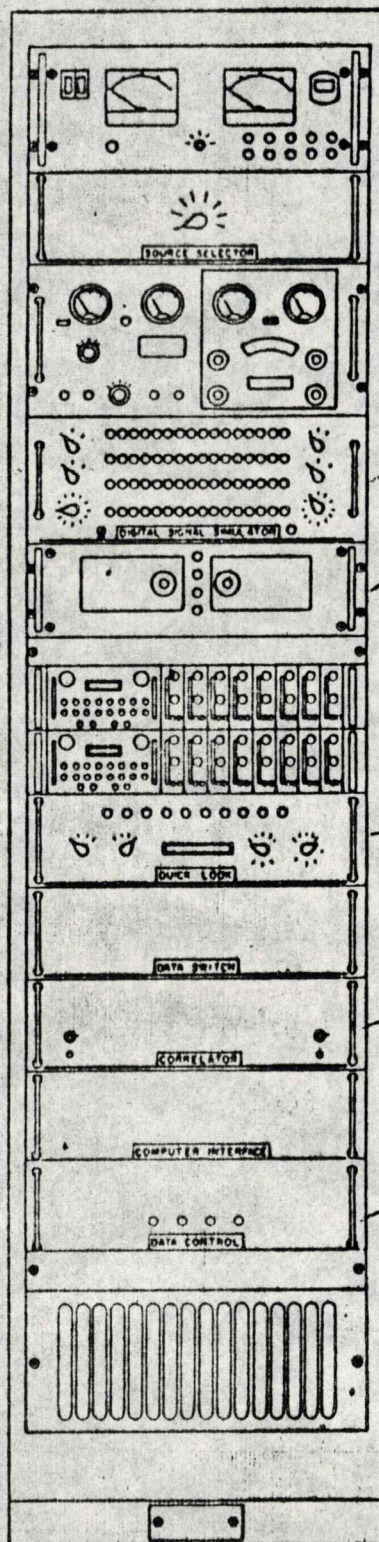
④ CORRELATOR PANEL ASSEMBLY

COMPUTER INTERFACE PANEL ASSEMBLY

⑤ DATA CONTROL PANEL ASSEMBLY

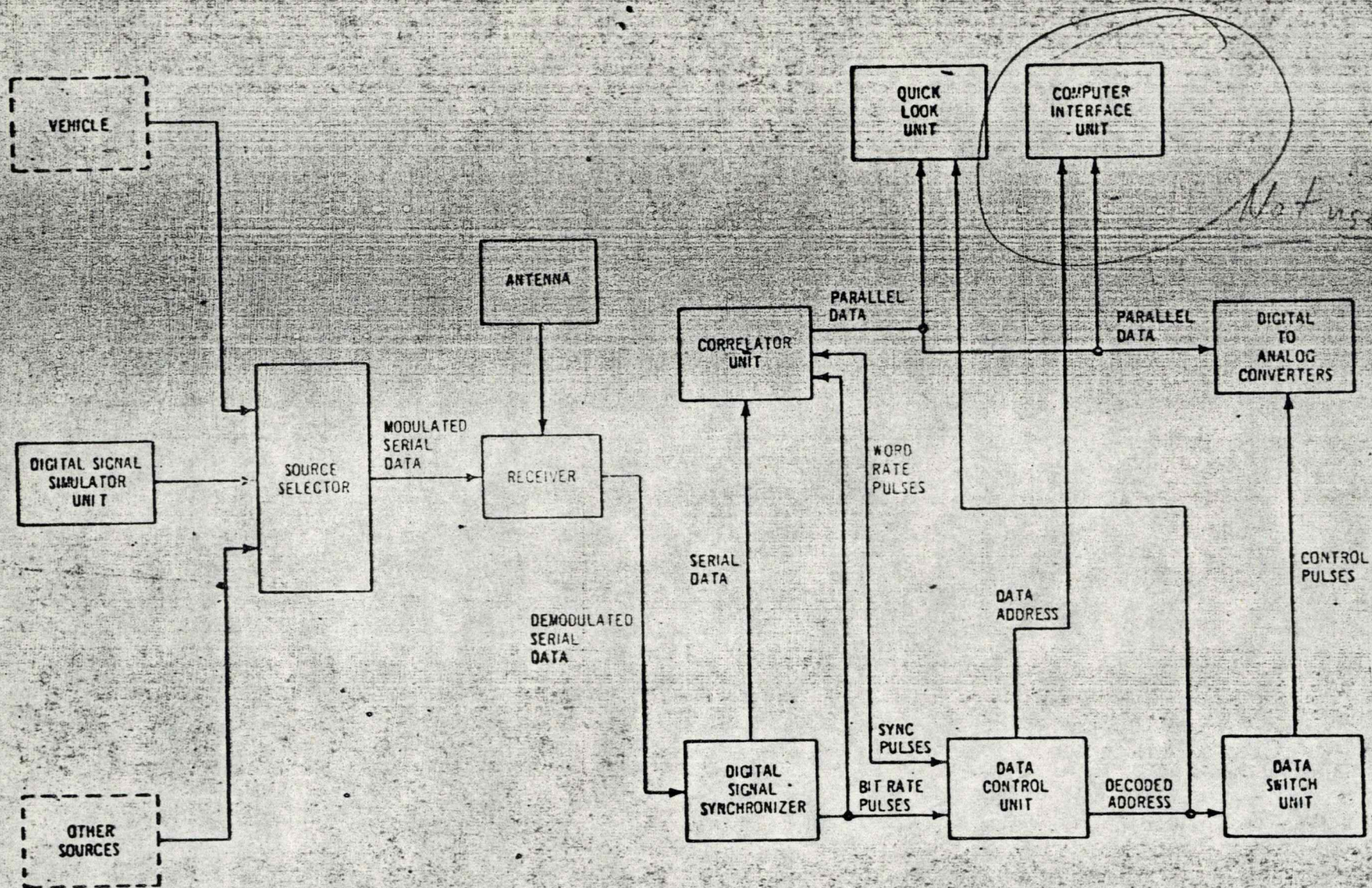
BLANK PANEL

BLOWER



C-10100-1

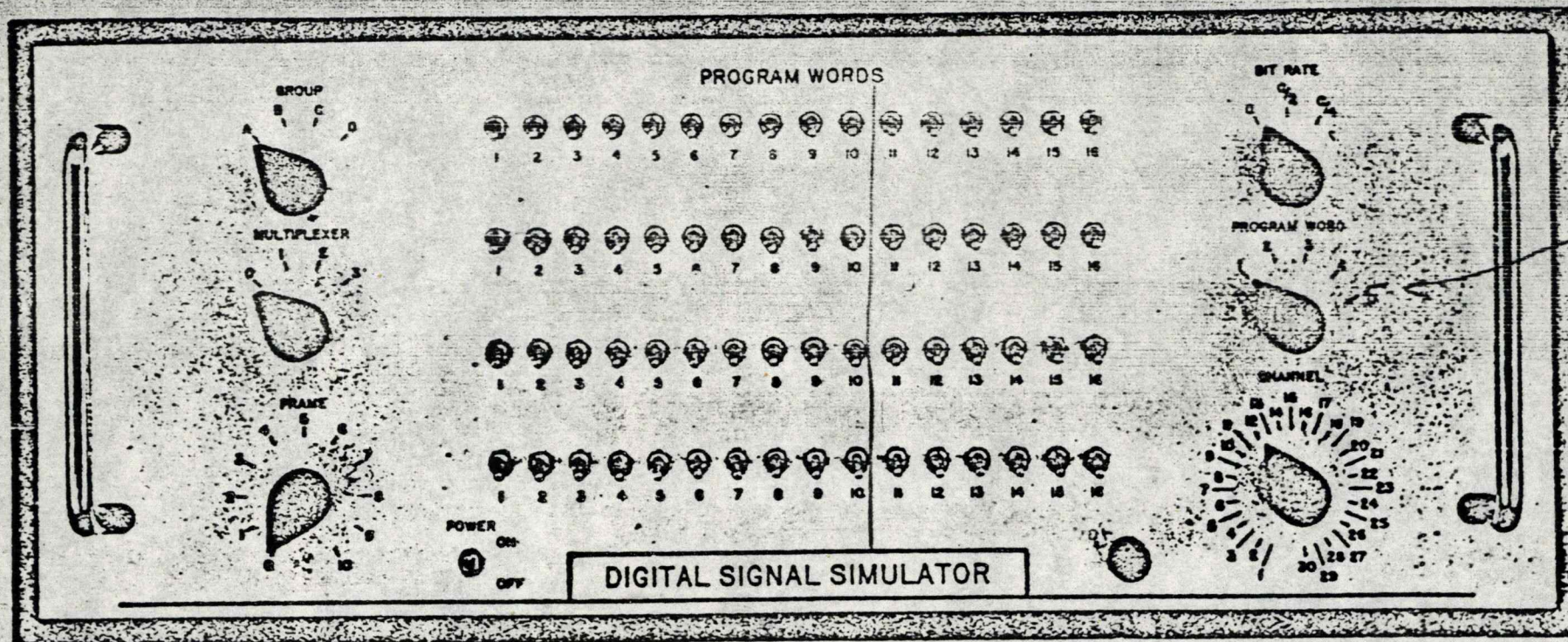
FIGURE 1-1. PCM/DDAS RECEIVING STATION MODEL DRS-1



CH 7276

FIGURE 1-2. DIGITAL DATA ACQUISITION SYSTEM (DDAS) DRS-1 BLOCK DIAGRAM

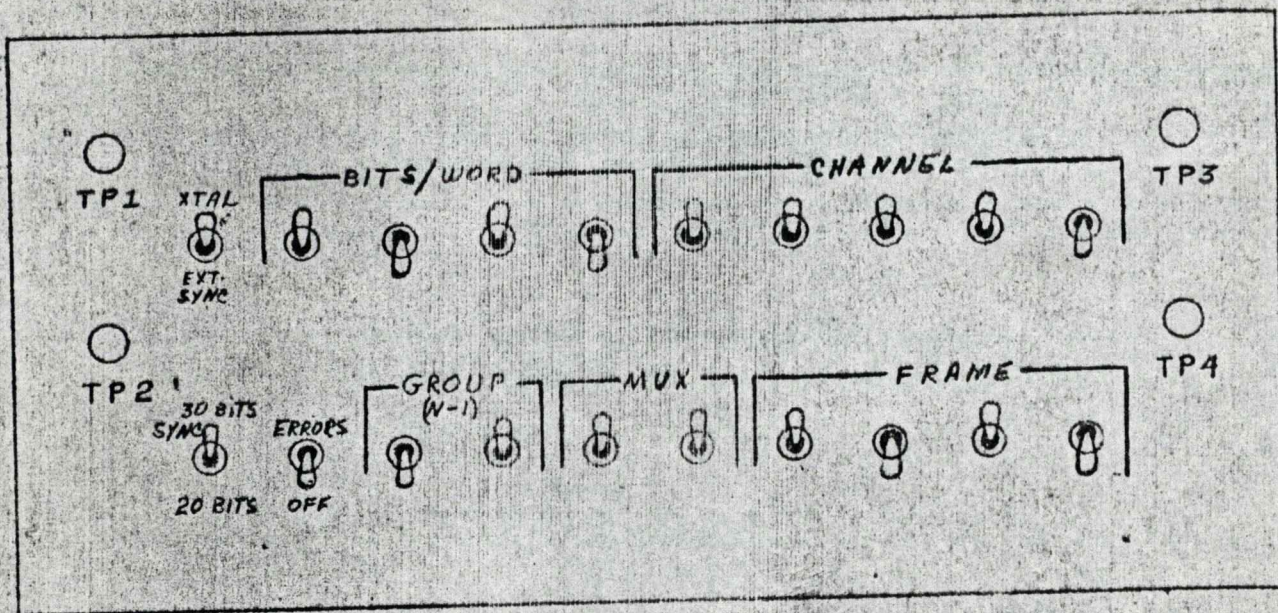
FOR TRAINING ONLY



Digital Signal Simulator

DIGITAL SIGNAL SIMULATOR

(CONTROL SWITCHES)



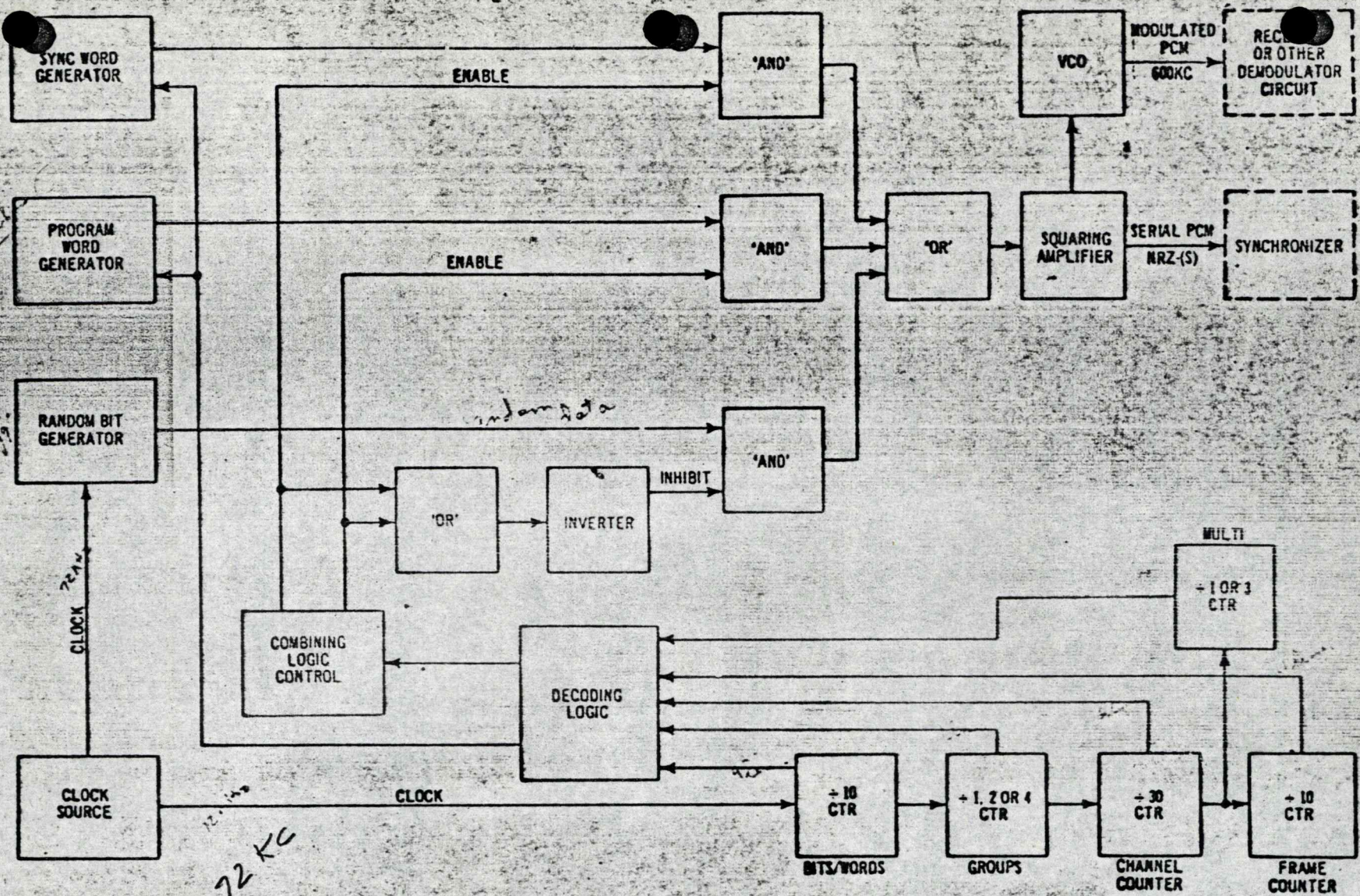
SETTINGS FOR 7200 WORD PER SECOND SATURN FORMAT:

1. XTAL-EXT. SYNC to XTAL
2. BITS/WORD to "1010"
3. CHAN COUNTER to "1110"
4. GROUPS to "01" (N-1)
5. MUX to "11"
6. FRAME COUNTER to "1010"
7. SYNC to 30 bits
8. SYNC ERRORS to OFF

ERRORS IN THE SYNC WORD WAVEFORM CAN BE ENTERED ON A CONTROLLED BASIS BY SETTING "SYNC ERRORS" TO "ON". IN THIS POSITION, THE FIRST 10 BITS OF THE SYNC WORDS ARE PROGRAMMED BY THE FIRST TEN SWITCHES OF THE FOURTH PROGRAM WORD. (BOTH FRAME & MASTER FRAME SYNC WORDS ARE CONTROLLED SIMULTANEOUSLY BY THIS PROCEDURE.)

1-20

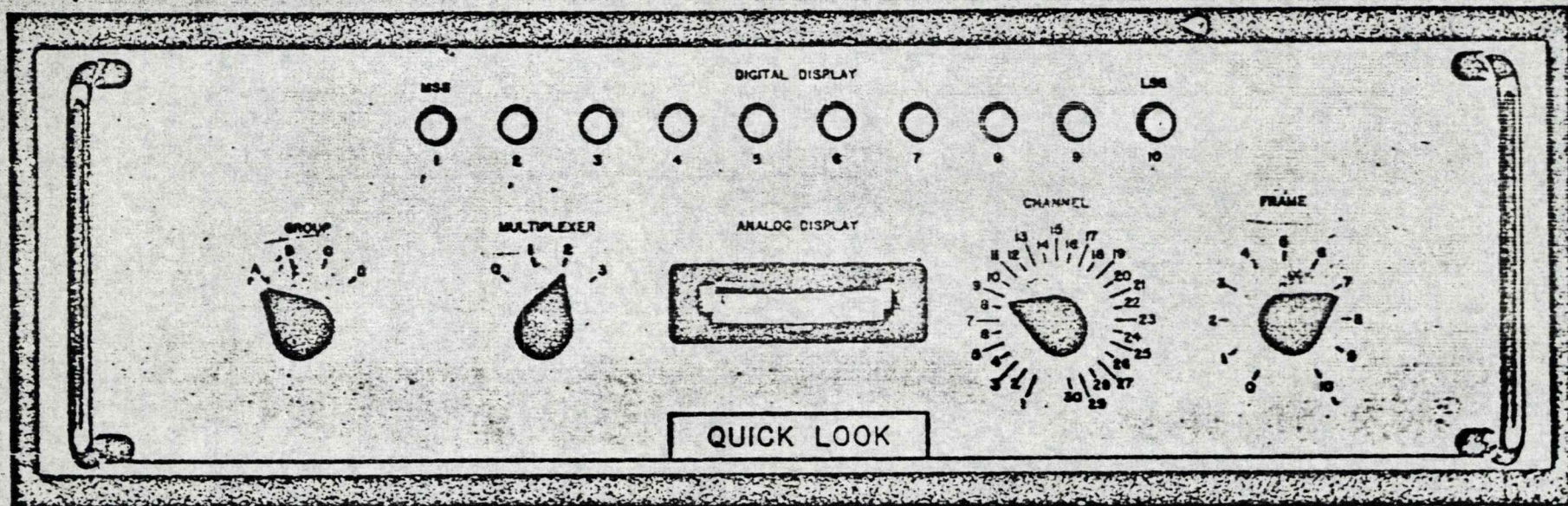
Revised December 15, 1964



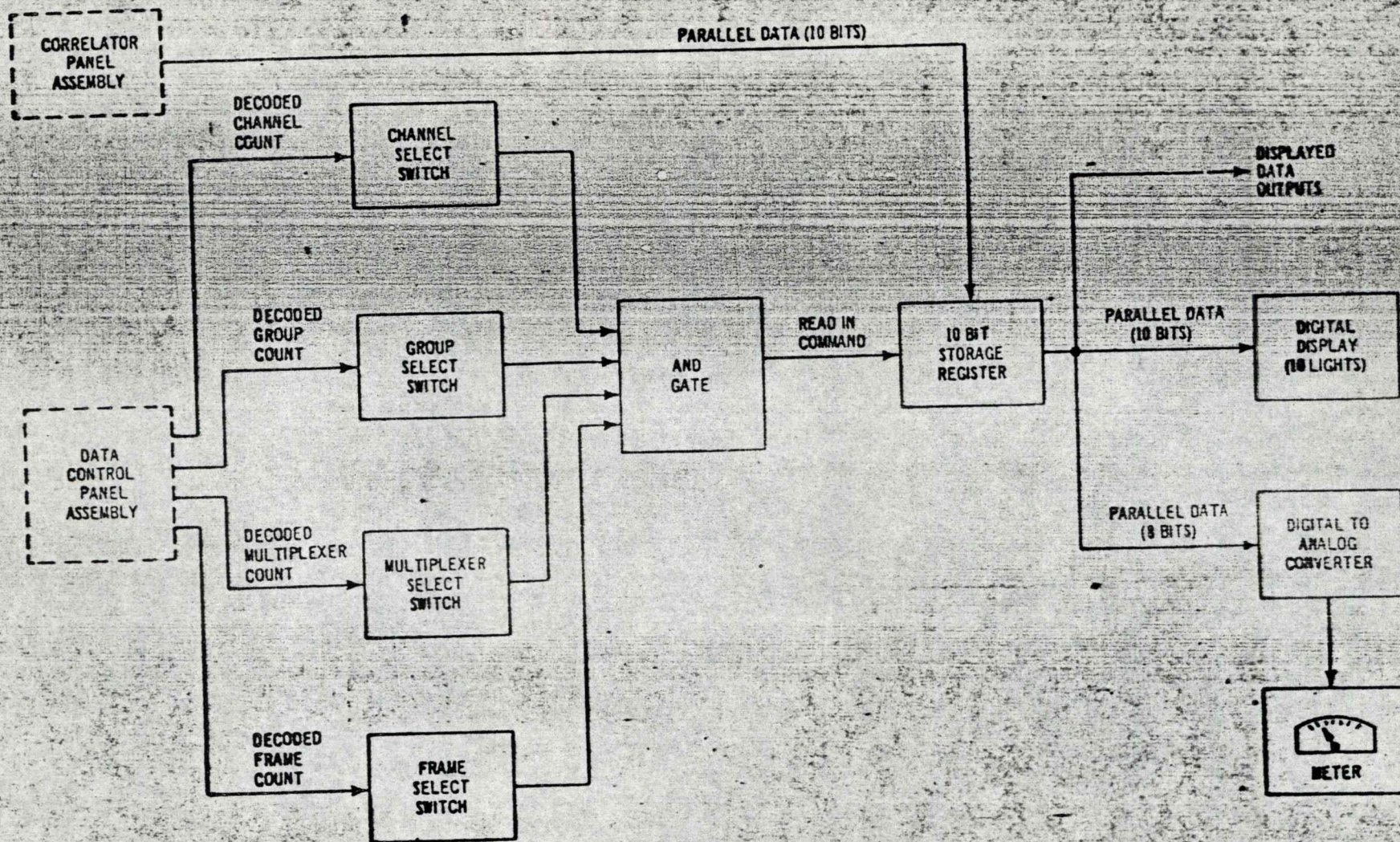
CH 8666

FIGURE 1-12. DIGITAL SIGNAL SIMULATOR BLOCK DIAGRAM

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Quick Look



CH 7308-1

FIGURE 1-9. QUICK LOOK PANEL ASSEMBLY BLOCK DIAGRAM

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DATA (TO BIT)

DECO
CHAN
COUNT

DECODED
GROUP
COUNT

DATA
COUNT

ACQUISITION

PROBATION

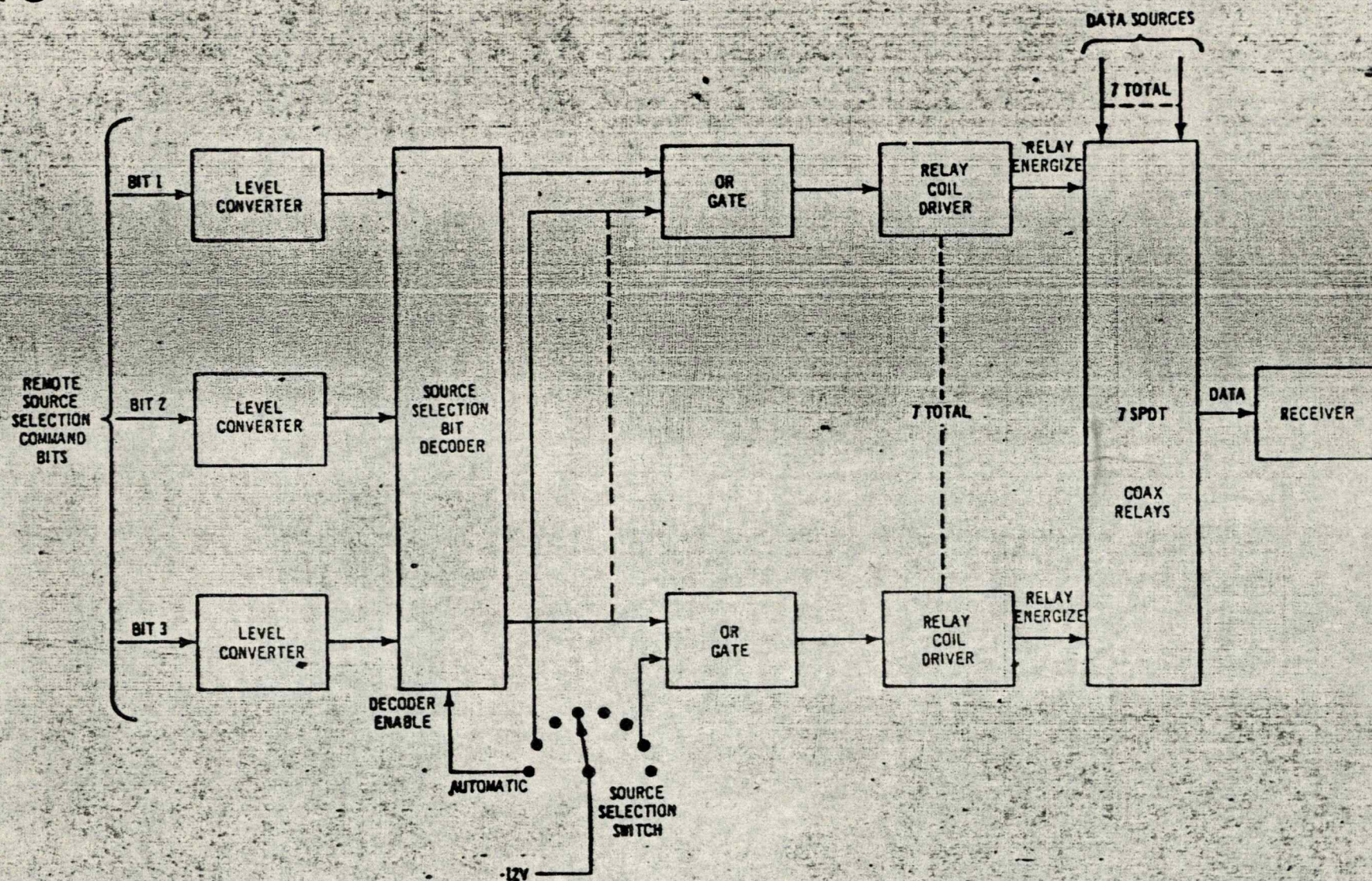
FRAME LOCK

MASTER
FRAME LOCK

DECODED
FRAME
COUNT

DATA CONTROL

Data Control



CM 7267

FIGURE 1-3. SOURCE SELECTOR PANEL ASSEMBLY BLOCK DIAGRAM

FOR TRAINING ONLY